
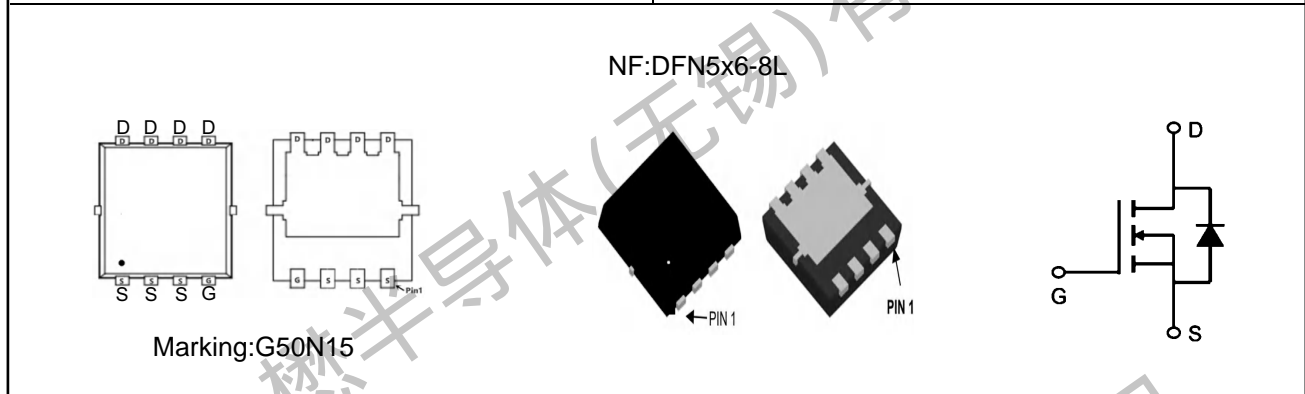


TMG50N15NF

N-Channel Enhancement Mosfet

<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>General Features</p> <p>$V_{DS} = 150V$ $I_D = 50A$</p> <p>$R_{DS(ON)} = 16m\Omega (typ.) @ V_{GS} = 10V$</p> <p>100% UIS Tested 100% R_g Tested</p> 
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Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	50	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	31.6	A
I_{DM}	Pulsed Drain Current ²	200	A
EAS	Single Pulse Avalanche Energy ³	135.2	mJ
I_{AS}	Avalanche Current	26	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation ⁴	104.1	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	3.6	$^\circ C/W$

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Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	150	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	---	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =20A	---	16	19.5	mΩ
		V _{GS} =4.5V, I _D =20A	---	---	---	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2.0	2.5	3.0	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	---	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =150V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =150V, V _{GS} =0V, T _J =100°C	---	---	100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =20A	---	50	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	2.3	---	Ω
Q _g	Total Gate Charge	V _{DS} =75V, V _{GS} =10V, I _D =20A	---	24.8	---	nC
Q _{gs}	Gate-Source Charge		---	7.5	---	
Q _{gd}	Gate-Drain Charge		---	4.2	---	
T _{d(on)}	Turn-On Delay Time	V _{DD} = 75V, I _D =20A, R _G = 3Ω, V _{GS} =10V	---	11.3	---	ns
T _r	Rise Time		---	5.2	---	
T _{d(off)}	Turn-Off Delay Time		---	21.2	---	
T _f	Fall Time		---	4.5	---	
C _{iss}	Input Capacitance	V _{DS} =75V, V _{GS} =0V, f=1MHz	---	1832	---	pF
C _{oss}	Output Capacitance		---	148	---	
C _{riss}	Reverse Transfer Capacitance		---	8.2	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	50	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =20A, T _J =25°C	---	---	1.2	V
t _{rr}	Reverse Recovery Time	I _F =20A, di/dt=100A / μs,	---	70	---	nS
Q _{rr}	Reverse Recovery Charge	T _J = 25 °C	---	154.7	---	nC

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The test condition is V_{DD}=25V, V_{GS}=10V, L=0.4mH, I_{AS}=26A.
3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

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Typical Characteristics

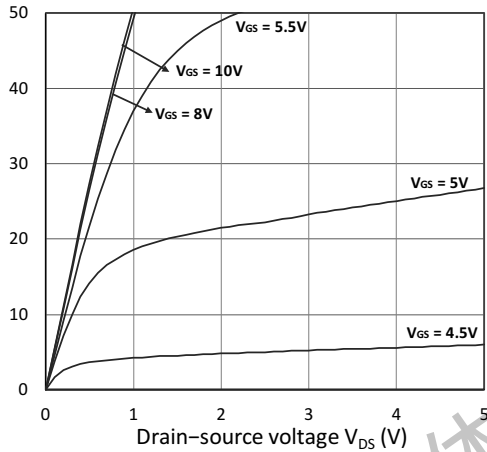


Figure 1. Output Characteristics

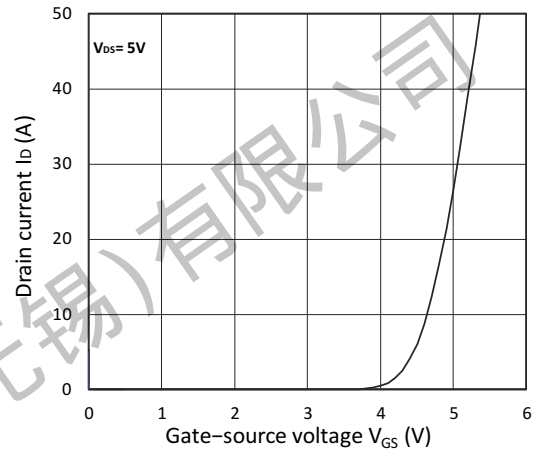


Figure 2. Transfer Characteristics

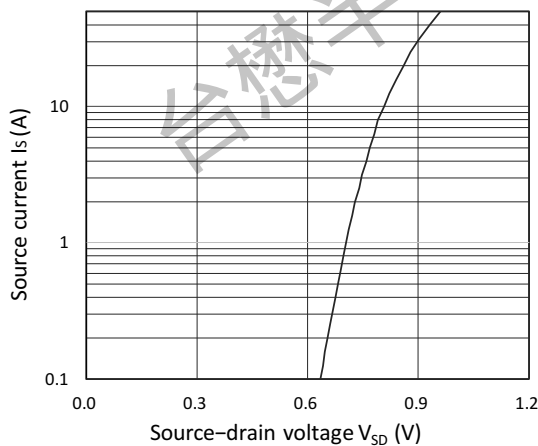


Figure 3. Forward Characteristics of Reverse

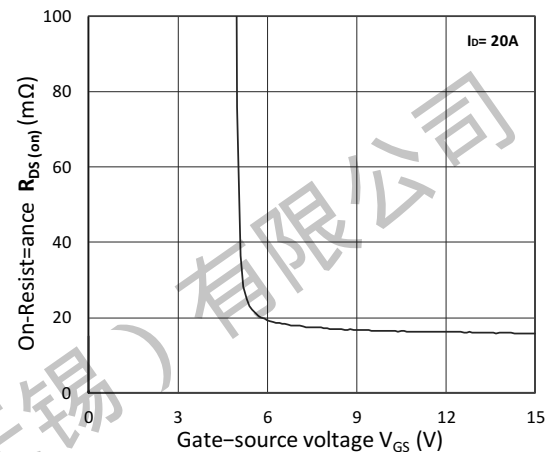


Figure 4. $R_{DS(on)}$ vs. V_{GS}

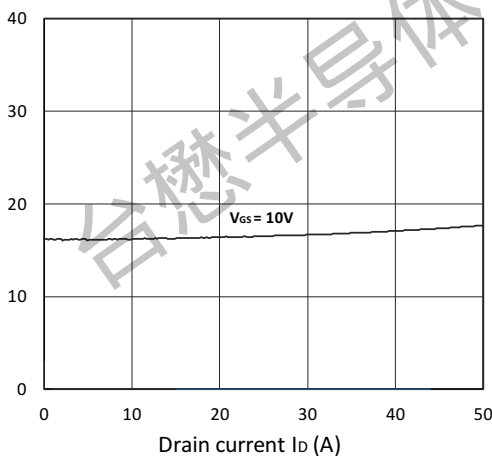


Figure 5. $R_{DS(on)}$ vs. I_D

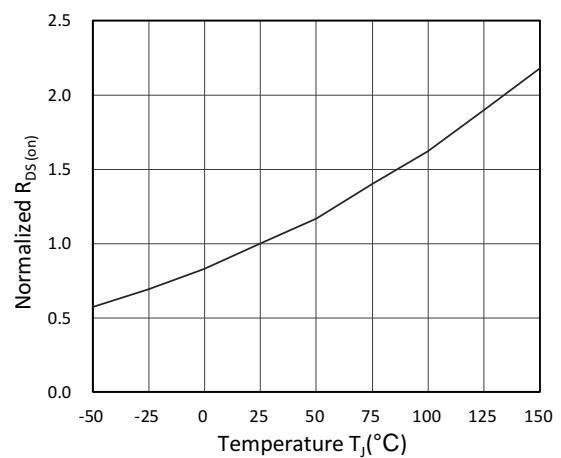


Figure 6. Normalized $R_{DS(on)}$ vs. Temperature

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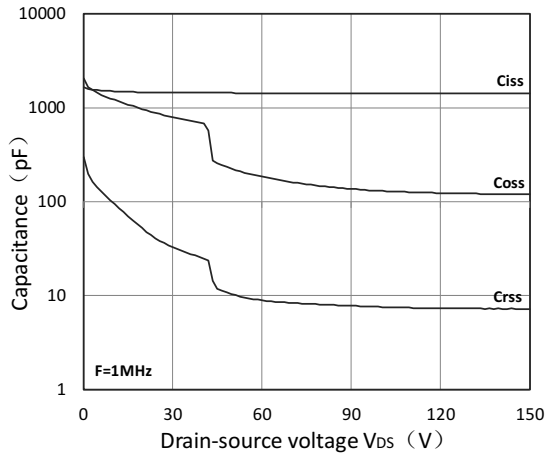


Figure 7. Capacitance Characteristics

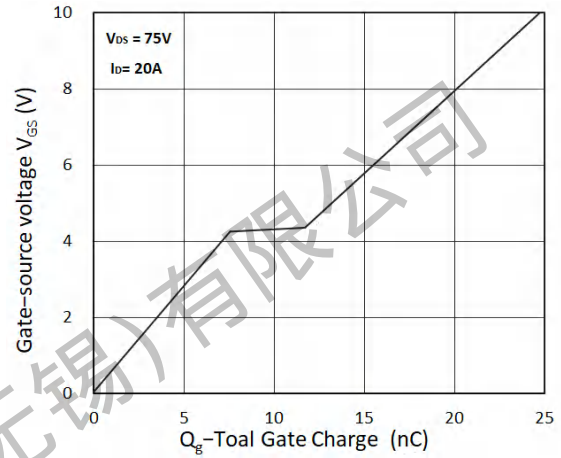


Figure 8. Gate Charge Characteristics

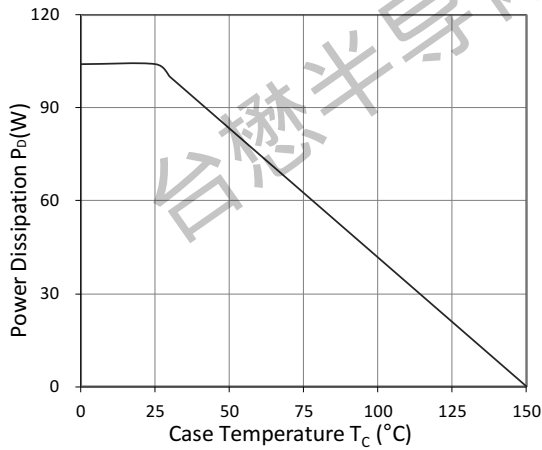


Figure 9. Power Dissipation

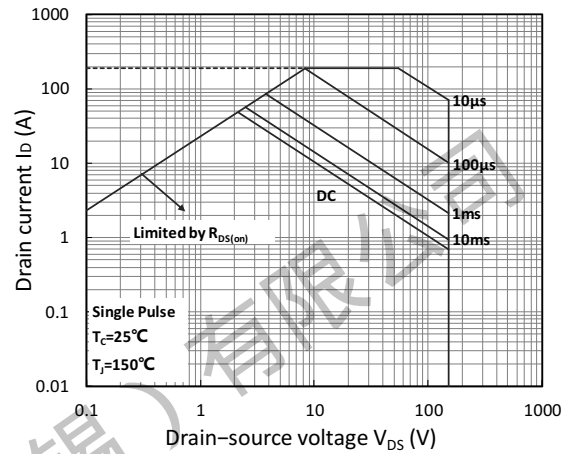


Figure 10. Safe Operating Area

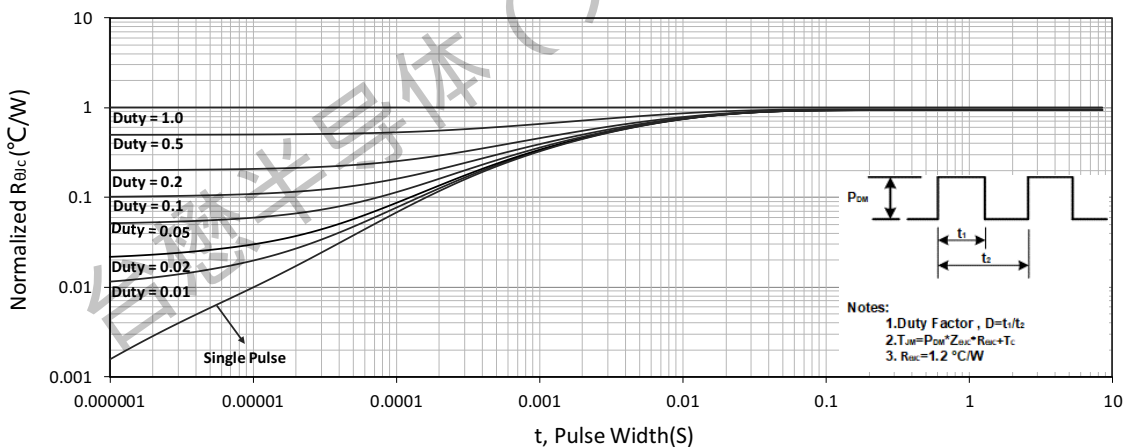
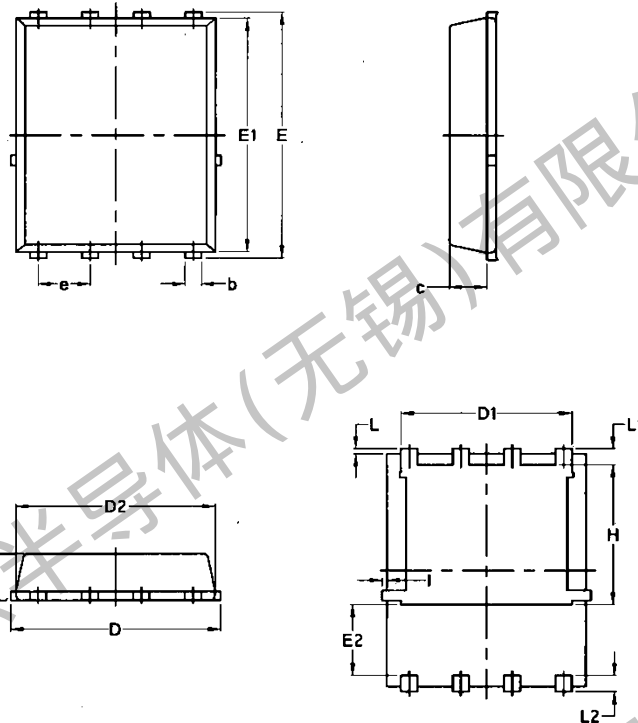


Figure 11. Normalized Maximum Transient Thermal Impedance

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Package Mechanical Data:DFN5x6-8L

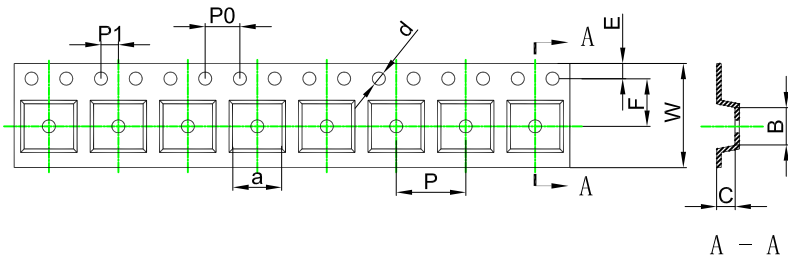


Symbol	Common			
	mm		Inch	
	Min	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
l	/	0.18	/	0.0070

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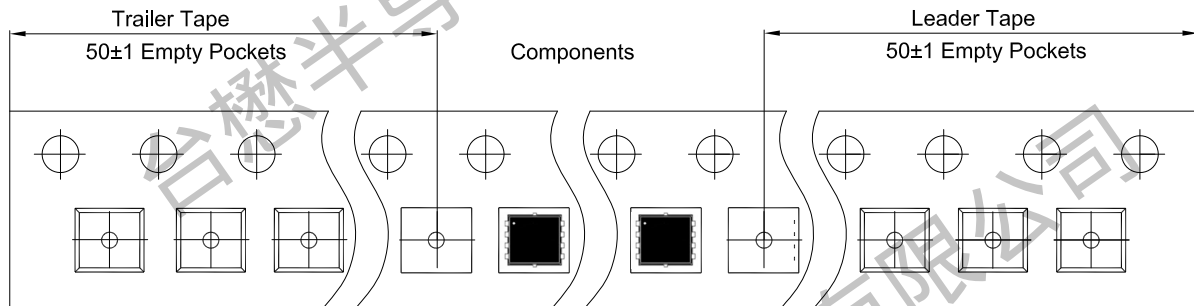
PDFN5x6-8L Embossed Carrier Tape



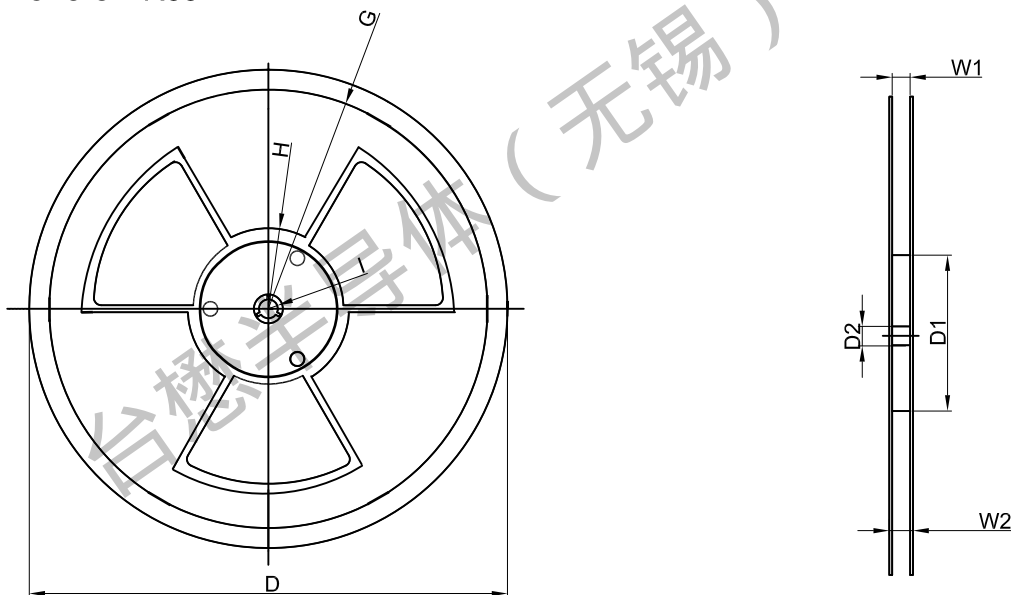
Packaging Description:
SOP-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 2,500 units per 13" or 33cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).
ALL DIM IN mm

Dimensions are in millimeter										
Pkg type	a	B	C	d	E	F	P0	P	P1	W
PDFN5x6-8L	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

PDFN5x6-8L Tape Leader and Trailer



PDFN5x6-8L Reel



Dimensions are in millimeter								
Reel Option	D	D1	D2	G	H	I	W1	W2
13" Dia	Ø330.00	100.00	13.00	R135.00	R55.00	R6.50	12.00	14.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
5,000 pcs	13 inch	10,000 pcs	370×355×52	50,000 pcs	400×360×368	

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Revision history:

Date	Rev	Description	Page
2024.05.14	24.05	Original	