



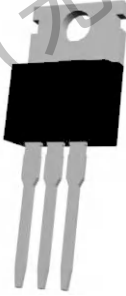
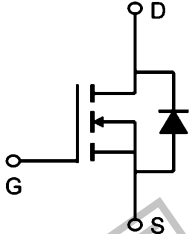


# TM150N12P

# N-Channel Enhancement Mosfet

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<p><b>General Features</b></p> <p><math>V_{DS} = 120V</math> <math>I_D = 150A</math>  <math>R_{DS(ON)} = 4.5m\Omega</math> (typ.) @ <math>V_{GS} = 10V</math></p> <p>100% UIS Tested          100% <math>R_g</math> Tested</p> 
--	--

P:TO-220AB

Marking: 150N12

G D S

**Absolute Maximum Ratings** ( $T_c = 25^\circ C$  unless otherwise noted)

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain-to-Source Voltage	120	V
$I_D$	Continuous Drain Current	$T_c = 25^\circ C$	150
	Continuous Drain Current	$T_c = 100^\circ C$	95
$I_{DM}$	Pulsed Drain Current	1050	A
$E_{AS}$	Single pulse avalanche energy	729	mJ
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$P_D$	Power Dissipation	208	W
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	175, -55 to 175	$^\circ C$

**Thermal Characteristics**

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.6	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	---	$^\circ C/W$

**TM150N12P**

**N-Channel Enhancement Mosfet**

**Electrical Characteristics** (Tc= 25°C unless otherwise specified) :

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	120	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 105V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	2.5	3	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 30A	-	4.5	6.5	mΩ
C <sub>iss</sub>	Input Capacitance		-	11122	-	pF
C <sub>OSS</sub>	Output Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 25V, f = 1MHz	-	507	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	430	-	pF
Q <sub>g</sub>	Total Gate Charge		-	150	-	nC
Q <sub>gs</sub>	Gate Source Charge	V <sub>GS</sub> = 0 to 10V V <sub>DS</sub> = 50V, I <sub>D</sub> = 70A	-	55	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	49	-	nC
t <sub>d(on)</sub>	Turn-On DelayTime		-	40	-	ns
t <sub>r</sub>	Turn-On Rise Time	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 50V	-	83	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime	I <sub>D</sub> = 70A, R <sub>GEN</sub> = 3Ω	-	78	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	51	-	ns
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	150	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	1050	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time		-	60	-	ns
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> = 70A, di/dt = 100A/us	-	110	-	nC

TM150N12P

N-Channel Enhancement Mosfet

Typical Electrical and Thermal Characteristics (Curves)

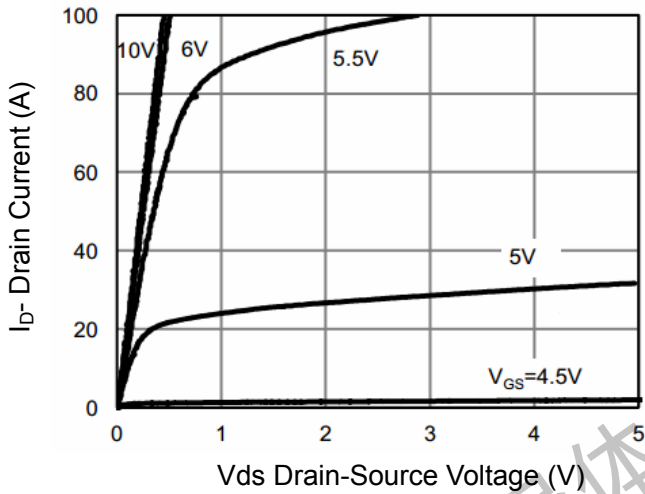


Figure 1 Output Characteristics

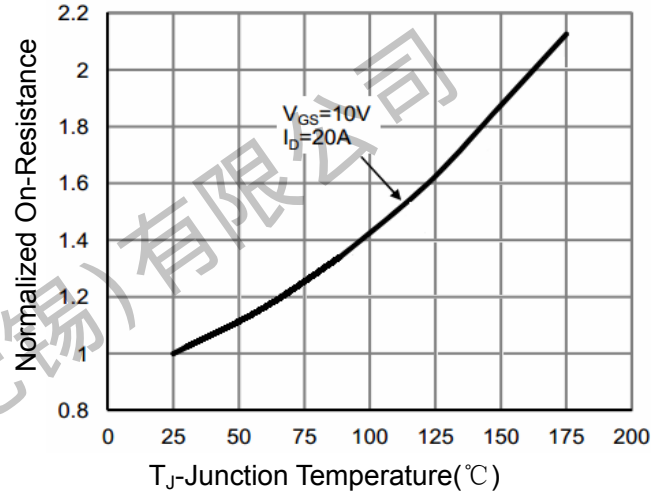


Figure 4 Rds(on)-Junction Temperature

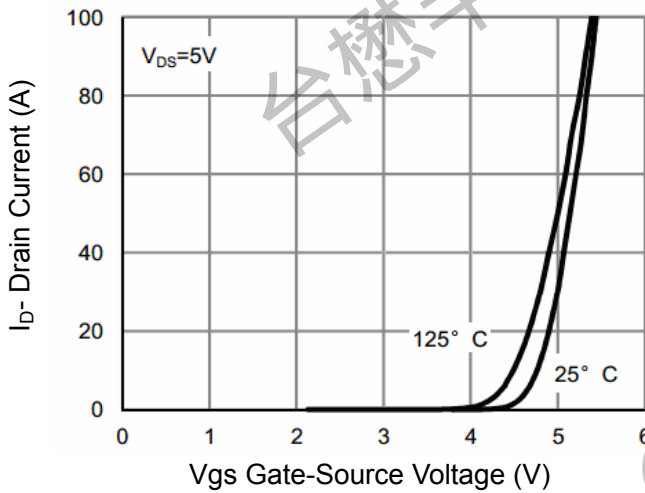


Figure 2 Transfer Characteristics

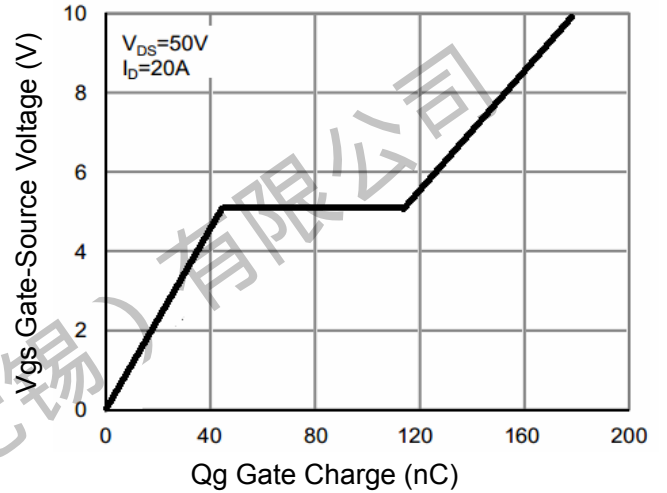


Figure 5 Gate Charge

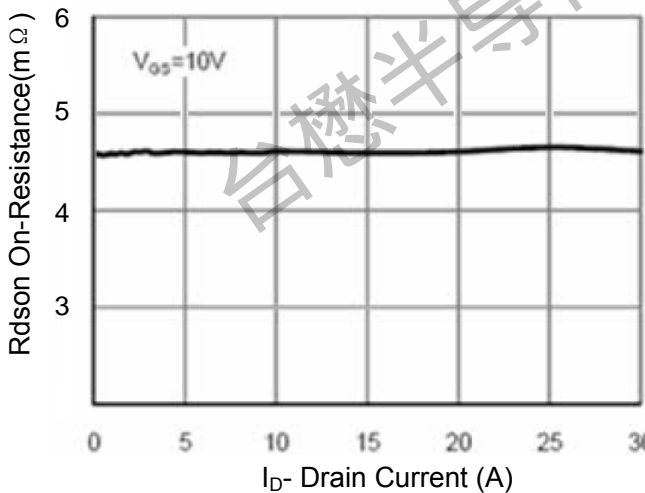


Figure 3 Rds(on)- Drain Current

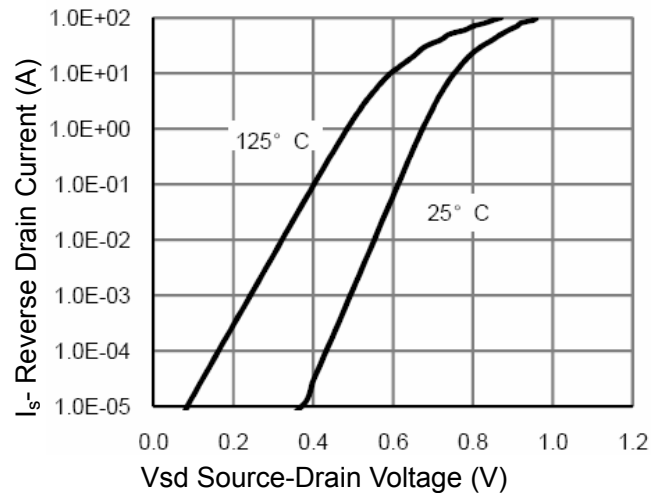
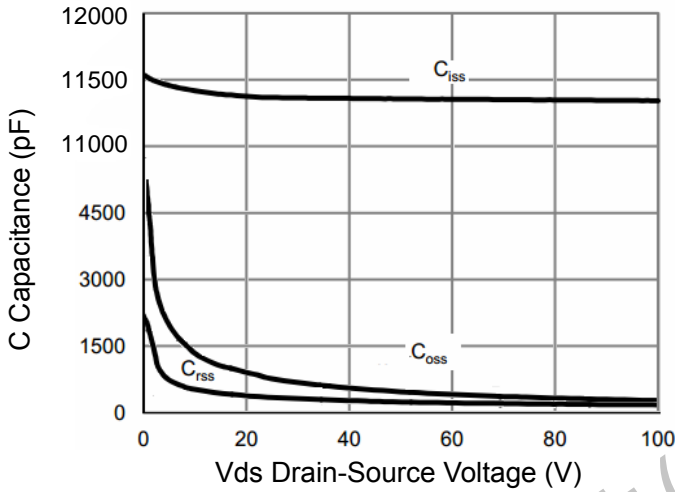


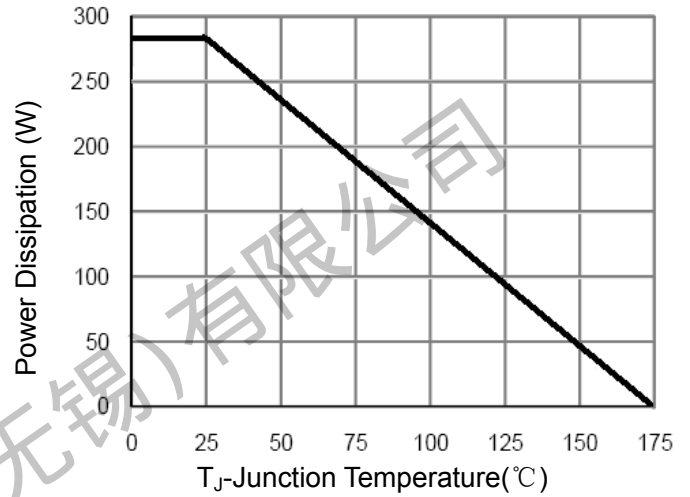
Figure 6 Source- Drain Diode Forward

**TM150N12P**

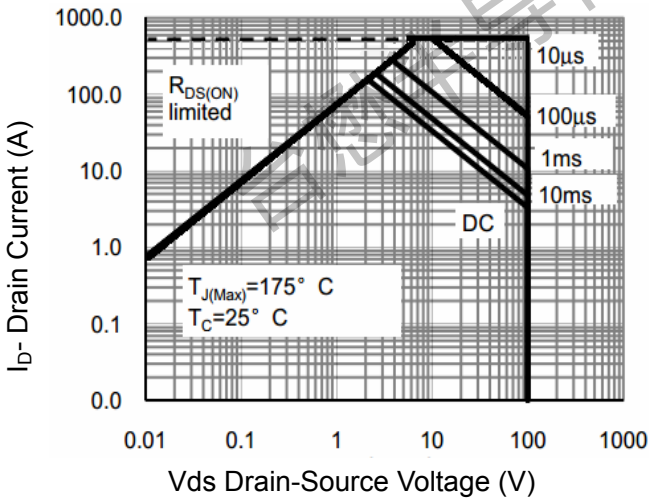
**N-Channel Enhancement Mosfet**



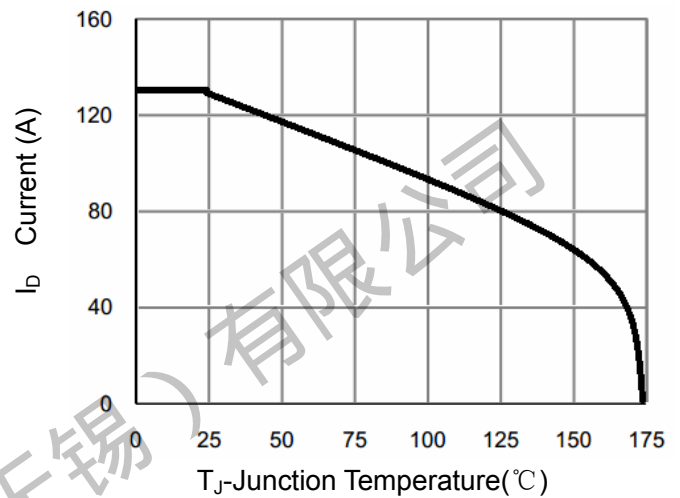
**Figure 7 Capacitance vs Vds**



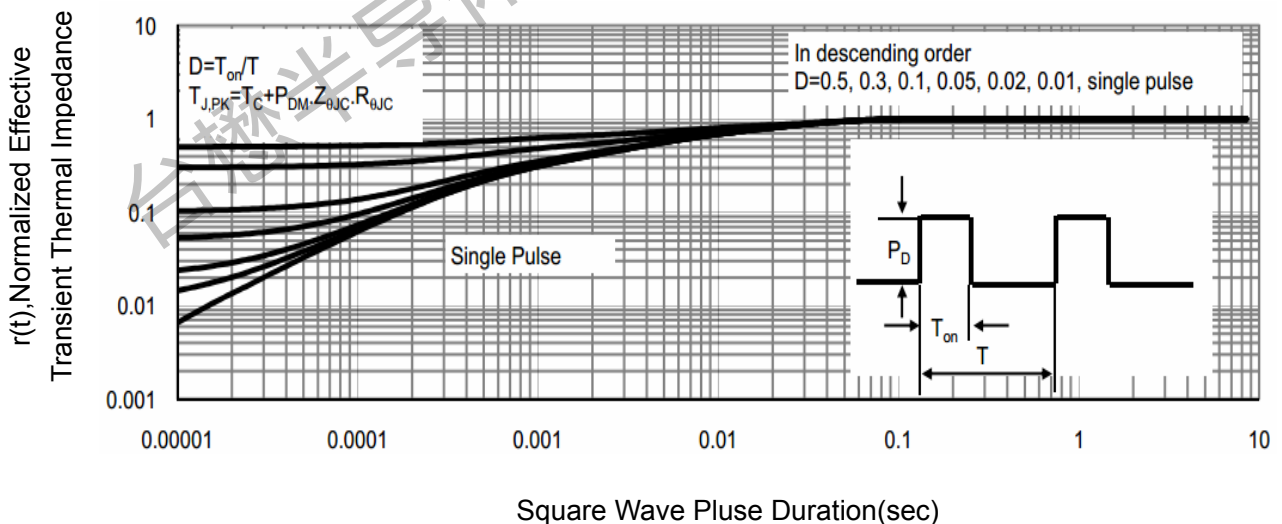
**Figure 9 Power De-rating**



**Figure 8 Safe Operation Area**



**Figure 10 ID Current- Junction Temperature**

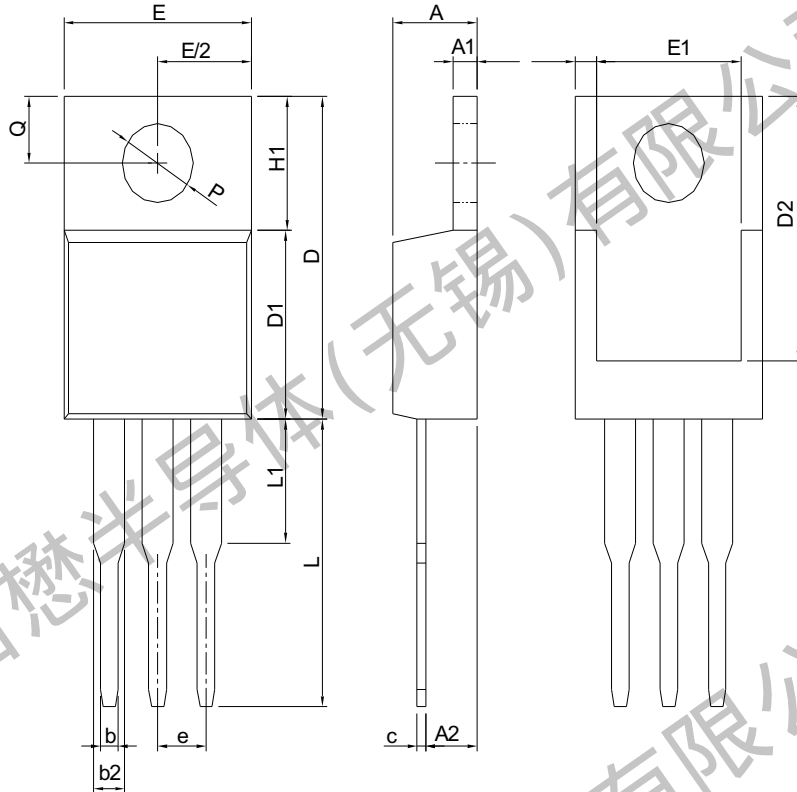


**Figure 11 Normalized Maximum Transient Thermal Impedance**

TM150N12P

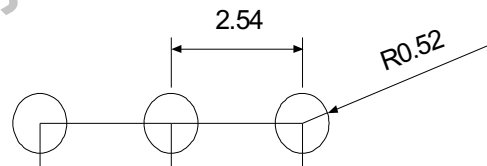
N-Channel Enhancement Mosfet

Package Mechanical Data: TO-220AB



SYMBOL	TO-220			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	3.56	4.83	0.140	0.190
A1	0.51	1.40	0.020	0.055
A2	2.03	2.92	0.080	0.115
b	0.38	1.02	0.015	0.040
b2	1.14	1.78	0.045	0.070
c	0.36	0.61	0.014	0.024
D	14.22	16.51	0.560	0.650
D1	8.38	9.02	0.330	0.355
D2	12.19	13.65	0.480	0.537
E	9.65	10.67	0.380	0.420
E1	6.86	8.89	0.270	0.350
e	2.54 BSC		0.100 BSC	
H1	5.84	6.86	0.230	0.270
L	12.70	14.73	0.500	0.580
L1	-	6.35	-	0.250
P	3.53	4.09	0.139	0.161
Q	2.54	3.43	0.100	0.135

RECOMMENDED LAND PATTERN

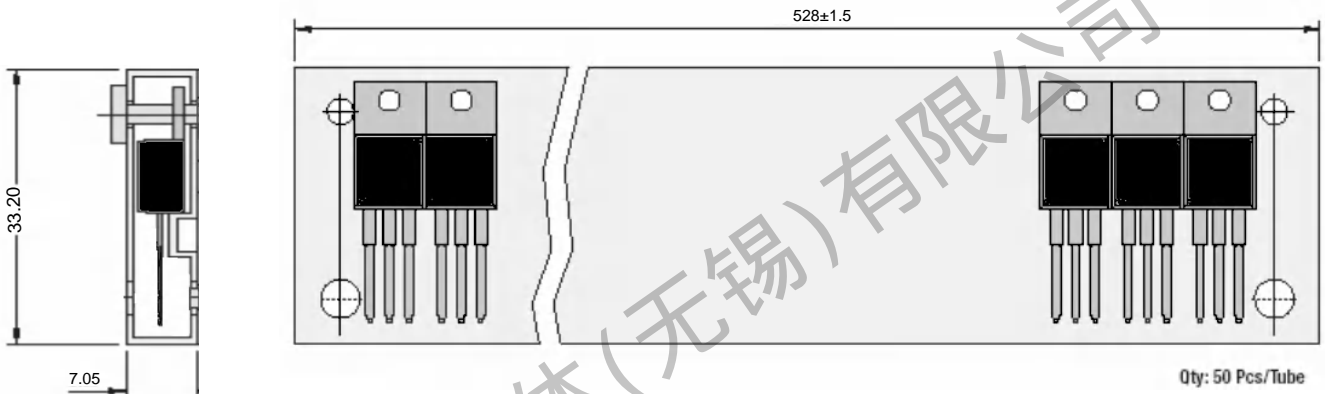


UNIT: mm

Note: Follow JEDEC TO-220 AB.

**TM150N12P**

**N-Channel Enhancement Mosfet**



All Dimensions are in mm

**1.TO-220AB Packaging**

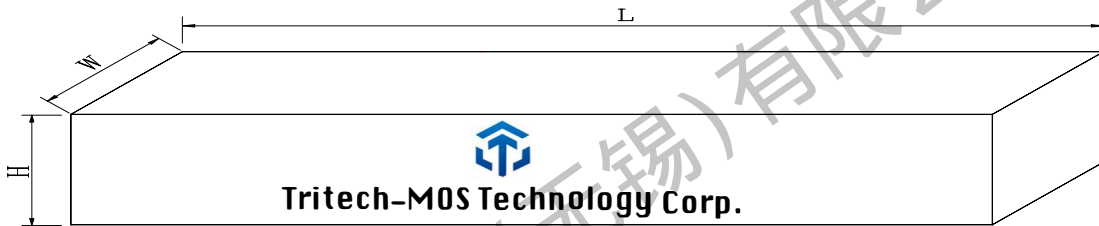
Package	Packing Form	Quantity		
		Tube	Inner Box [kpcs]	Outbox [kpcs]
TO-220AB	Tube Tape	50	5	1



# TM150N12P

# N-Channel Enhancement Mosfet

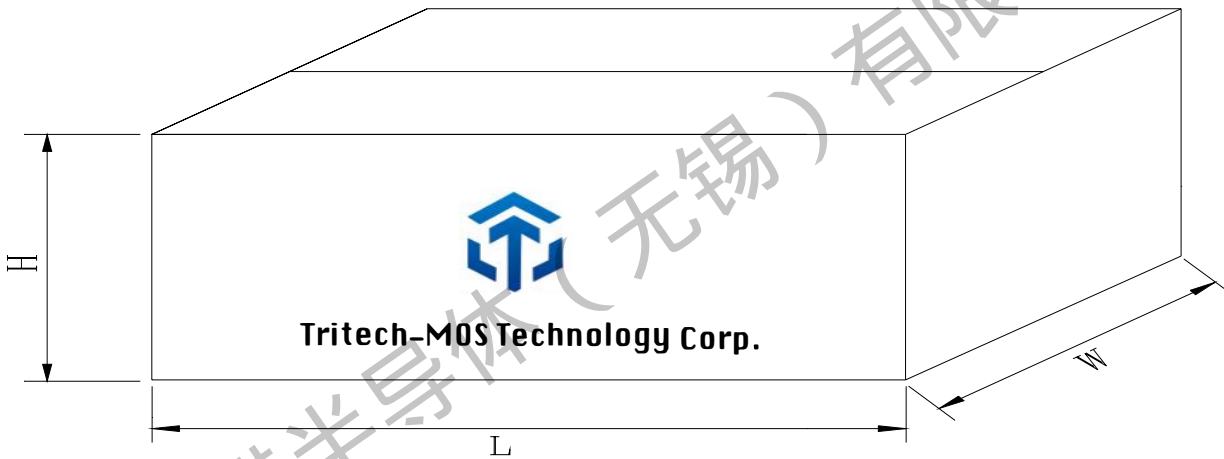
## Inner Box



Dimension : 580 (L)×154(W) ×49(H) mm

Quantity : 50 ×20Ea = 1Kpcs

## Outer Box



Dimension : 595(L)×285(W) ×185(H) mm

Quantity : 1K×5Ea = 5Kpcs



## TM150N12P

## N-Channel Enhancement Mosfet

### Important Notices and Disclaimers

- Tritech-MOS Technology Corp. reserves the right to change this document, its products, and specifications at any time without prior notice.
- Before final design, purchase, or use, customers should obtain and confirm the latest product information and specifications.
- Tritech-MOS Technology Corp. makes no warranties, representations or warranties regarding the suitability of its products for any specific purpose, and Tritech-MOS Technology Corp. does not assume any responsibility for application assistance or customer product design.
- Tritech-MOS Technology Corp. does not guarantee or assume any responsibility for the purchase or use of any unexpected or unauthorized products.
- Any intellectual property rights of Tritech-MOS Technology Corp. are not licensed through implicate or other means.
- Products of Tritech-MOS Technology Corp. are not included as critical components in life support equipment or systems without explicit written approval from Tritech-MOS Technology Corp.

Revision history:

Date	Rev	Description	Page
2023.08.08	23.08	Original	