



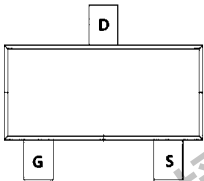
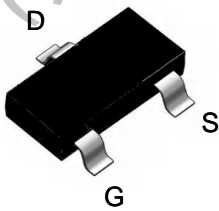
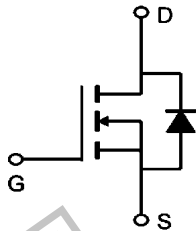
# TM08N018I

# N-Channel Enhancement Mosfet

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<p><b>General Features</b></p> <p><math>V_{DS}=18V</math> <math>I_D=8A</math>  <math>R_{DS(ON)} = 13m\Omega</math> (Typ.) @ <math>V_{GS}=4.5V</math></p> <p>100% UIS Tested          100% <math>R_g</math> Tested</p>
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I:SOT-23

Marking: 1808

**Absolute Maximum Ratings ( $T_C=25^\circ C$  unless otherwise noted)**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	18	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	8	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	6	A
$I_{DM}$	Pulsed Drain Current	40	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation	1.4	W
$T_{STG}$	Storage Temperature Range	-55 to 175	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 175	$^\circ C$

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	46	$^\circ C/W$
$R_{JC}$	Thermal Resistance Junction Case	---	---	$^\circ C/W$

**TM08N018I**
**N-Channel Enhancement Mosfet**
**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	18	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	$\mu\text{A}$
$I_{GSS}$	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 12\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.5	0.7	0.9	V
$R_{DS(ON)}$	Static Drain-Source ON-Resistance	$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$	-	13	16	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 5\text{A}$	-	17	20	$\text{m}\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{MHz}$	-	665	-	pF
$C_{oss}$	Output Capacitance		-	90	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	80	-	pF
$Q_g$	Total Gate Charge	$V_{GS} = 0 \text{ to } 4.5\text{V}$ $V_{DD} = 10\text{V}, I_D = 5\text{A}$	-	13	-	nC
$Q_{gs}$	Gate Source Charge		-	2	-	nC
$Q_{gd}$	Gate Drain ("Miller") Charge		-	3	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{GS} = 4.5\text{V}, V_{DD} = 10\text{V}$ $I_D = 5\text{A}, R_{GEN} = 3\Omega$	-	7	-	ns
$t_r$	Turn-On Rise Time		-	25	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	34	-	ns
$t_f$	Turn-Off Fall Time		-	13	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
$I_S$	Maximum Continuous Drain to Source Diode Forward Current		-	-	8	A
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current		-	-	40	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}, I_S = 10\text{A}$	-	-	1.2	V
$t_{rr}$	Body Diode Reverse Recovery Time	$I_F = 5\text{A}, di/dt = 100\text{A}/\mu\text{s}$	-	10	-	ns
$Q_{rr}$	Body Diode Reverse Recovery Charge		-	2.7	-	nC

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**Typical Performance Characteristics**

Figure 1: Output Characteristics

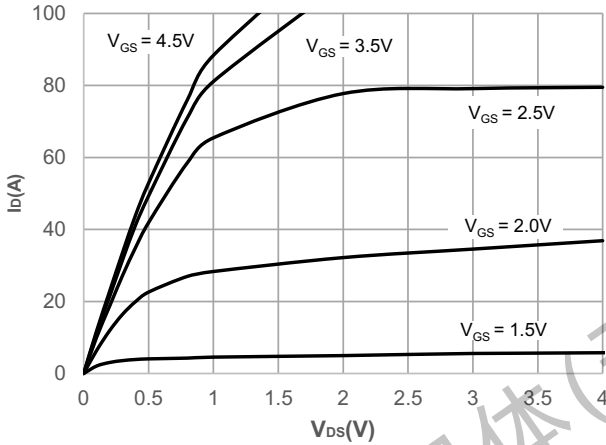


Figure 2: Typical Transfer Characteristics

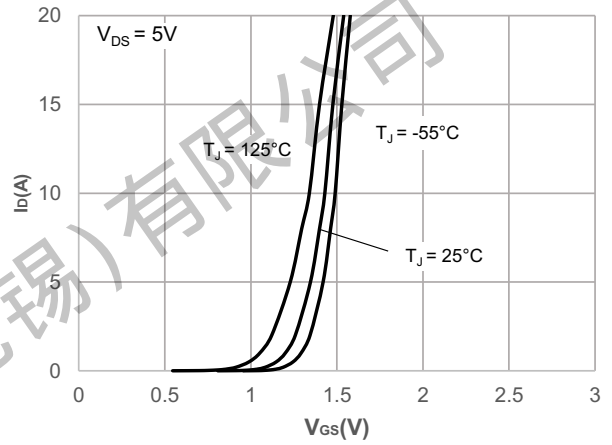


Figure 3: On-resistance vs. Drain Current

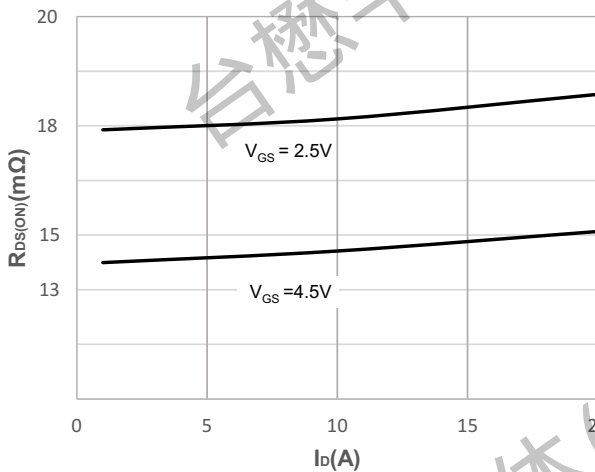


Figure 4: Body Diode Characteristics

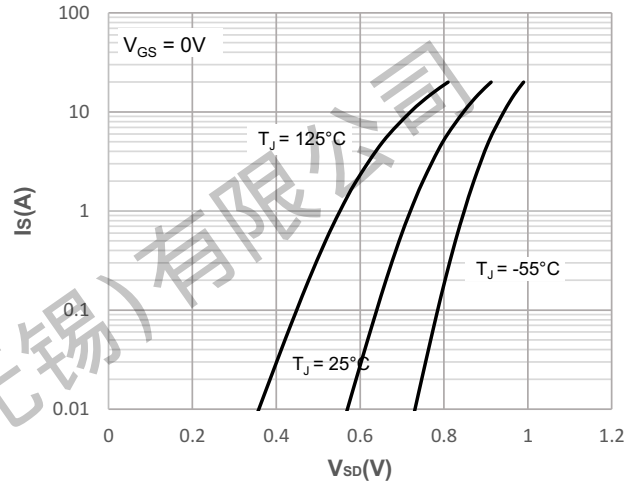


Figure 5: Gate Charge Characteristics

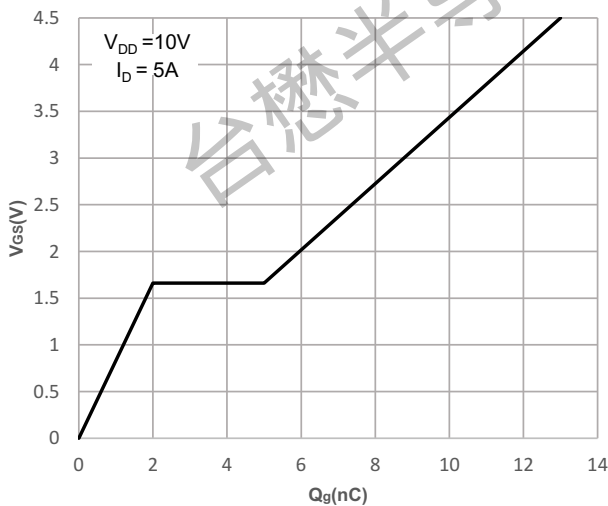
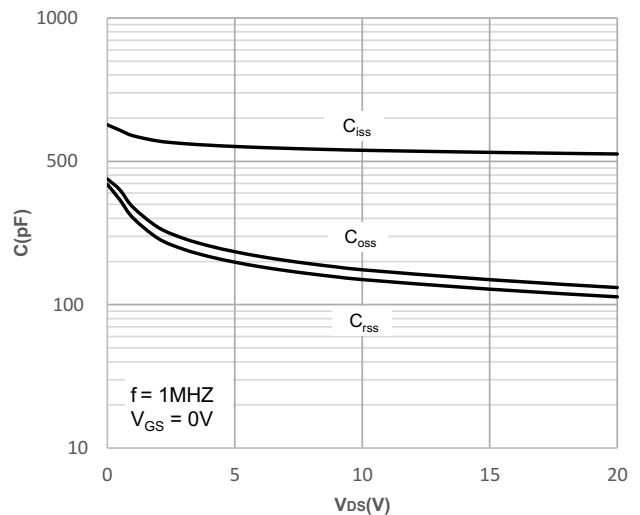


Figure 6: Capacitance Characteristics

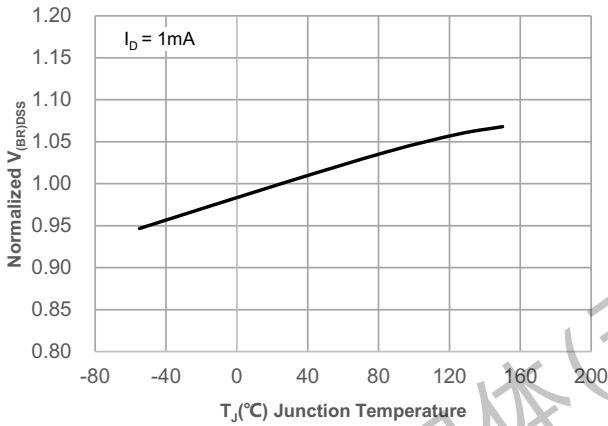


**TM08N0181**

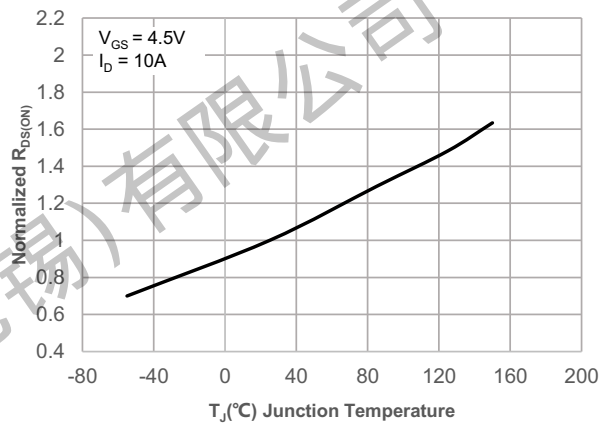
**N-Channel Enhancement Mosfet**

**Typical Performance Characteristics**

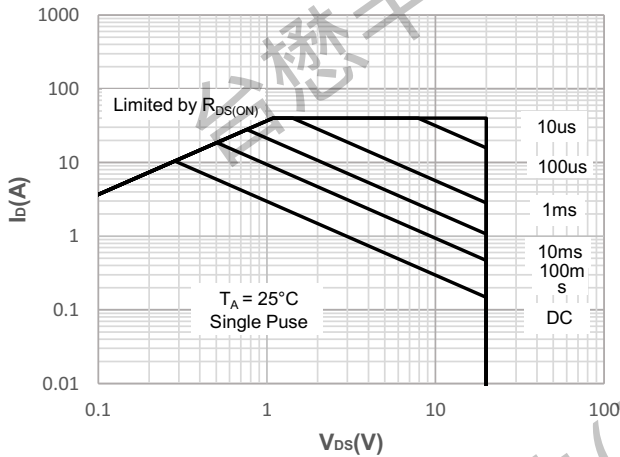
**Figure 7: Normalized Breakdown voltage vs. Junction Temperature**



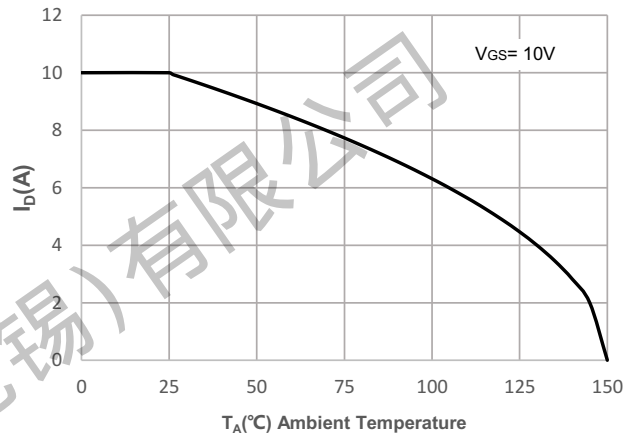
**Figure 8: Normalized on Resistance vs. Junction Temperature**



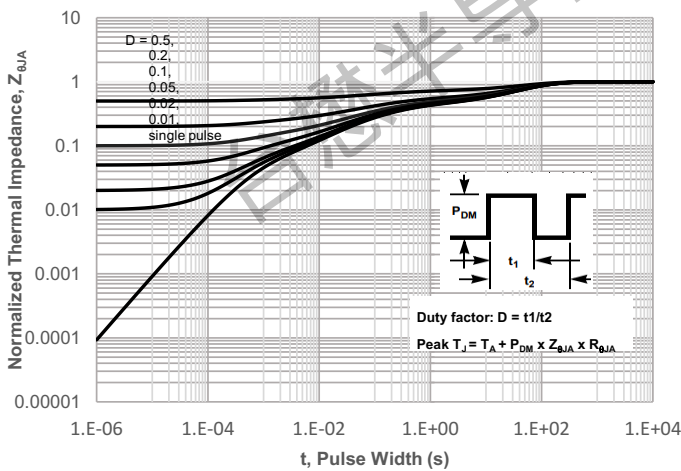
**Figure 9: Maximum Safe Operating Area**



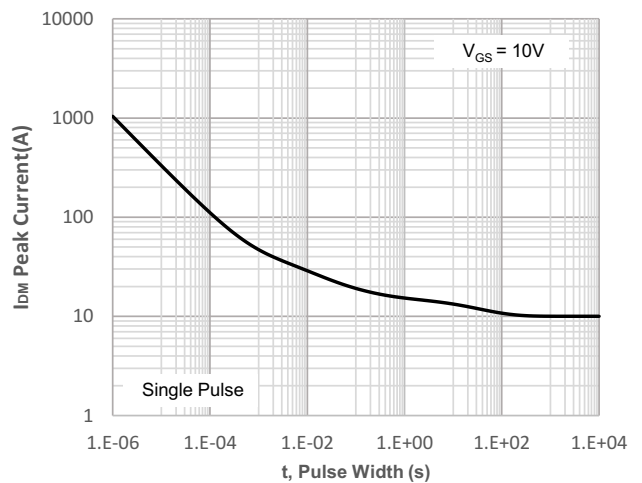
**Figure 10: Maximum Continuous Drianc Current vs. Ambient Temperature**



**Figure 11: Normalized Maximum Transient Thermal Impedance**



**Figure 12: Peak Current Capacity**

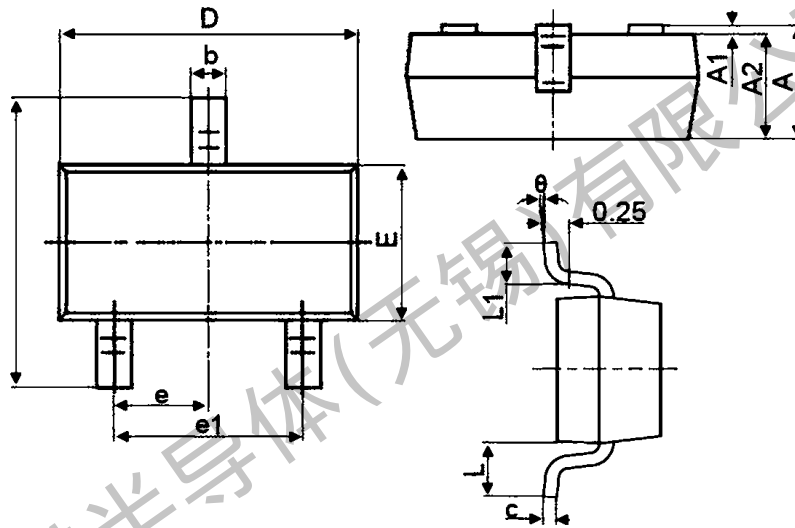




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**Package Mechanical Data:SOT-23**



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

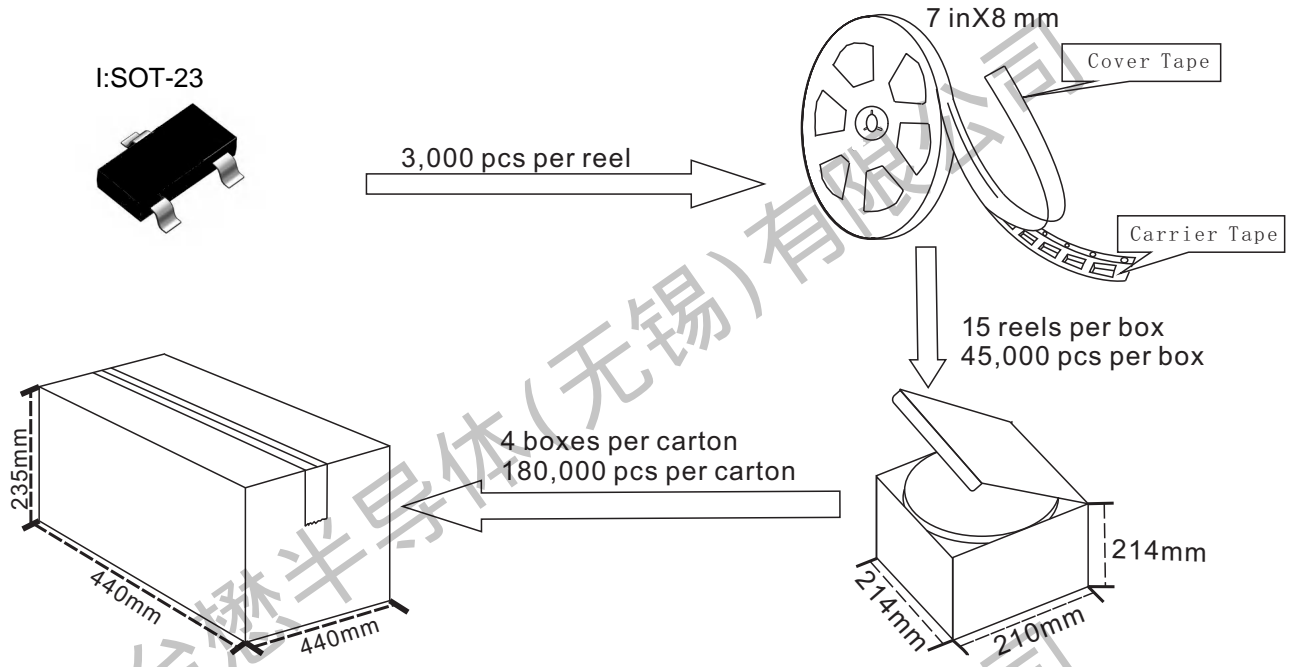


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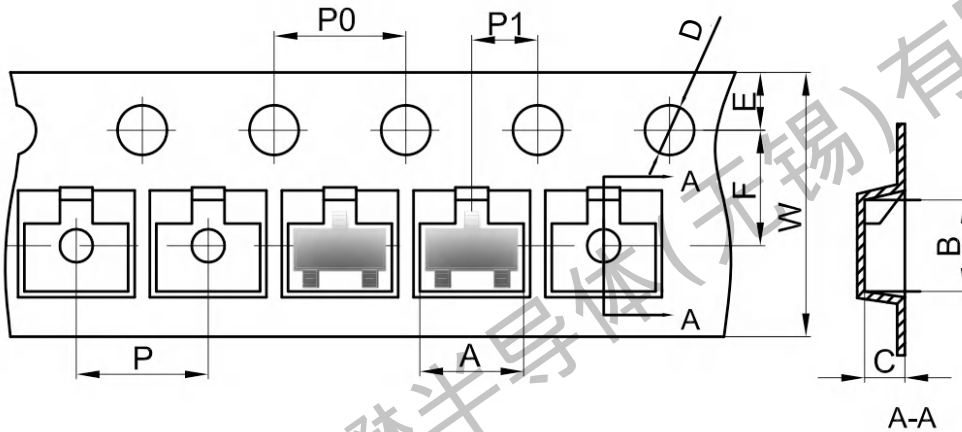
# N-Channel Enhancement Mosfet

## SOT-23 Packing

1. The method of packaging and dimension are shown as below figure. (Dimension in mm)



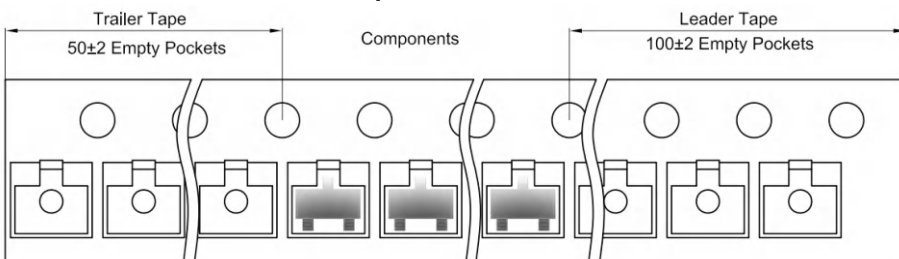
## SOT-23 Embossed Carrier Tape



Dimensions are in millimeter

Pkg type	A	B	C	D	E	F	P0	P	P1	W
SOT-23	3.15	2.77	1.22	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00

## SOT-23 Tape Leader and Trailer





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Revision history:

Date	Rev	Description	Page
2023.08.09	23.08	Original	