


TM07H02TS

N+N-Channel Enhancement Mode MOSFET

<p>General Description</p> <ul style="list-style-type: none"> • Low $R_{DS(ON)}$ • RoHS and Halogen-Free Compliant <p>Applications</p> <ul style="list-style-type: none"> • Load switch • PWM 	<p>General Features</p> <p>$V_{DS} = 20V$ $I_D = 7.0 A$ $R_{DS(ON)} = 19m\Omega$ (typ) @ $V_{GS} = 4.5V$</p> <p>100% UIS Tested 100% R_g Tested</p> 
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TS:TSSOP-8L

D1/D2	1	8	D1/D2
S1	2	7	S2
S1	3	6	S2
G1	4	5	G2

Marking: 8205A

Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	7.0	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 4.5V$	---	A
I_{DM}	Pulsed Drain Current	25	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation	1.25	W
T_{STG}	Storage Temperature Range	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	---	100	$^\circ C/W$

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Electrical Characteristics ($T_J=25$ unless otherwise specified)

Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	± 100	nA
On Characteristics						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.7	0.9	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=4.5V, I_D=4A$	-	19	27	m Ω
		$V_{GS}=2.5V, I_D=3A$	-	24	35	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=4A$	-	10	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=8V, V_{GS}=0V,$ $F=1.0MHz$	-	600	-	PF
Output Capacitance	C_{oss}		-	330	-	PF
Reverse Transfer Capacitance	C_{rss}		-	140	-	PF
Switching Characteristics						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=1A$ $V_{GS}=4V, R_{GEN}=10\Omega$	-	18	-	nS
Turn-on Rise Time	t_r		-	5	-	nS
Turn-Off Delay Time	$t_{d(off)}$		-	43	-	nS
Turn-Off Fall Time	t_f		-	20	-	nS
Total Gate Charge	Q_g	$V_{DS}=10V, I_D=4A,$ $V_{GS}=4.5V$	-	11	-	nC
Gate-Source Charge	Q_{gs}		-	2.3	-	nC
Gate-Drain Charge	Q_{gd}		-	2.5	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=2A$	-	0.8	1.2	V
Diode Forward Current	I_S		-	-	7.0	A

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Dual N-ch 20V Fast Switching MOSFETs

Typical Electrical and Thermal Characteristics

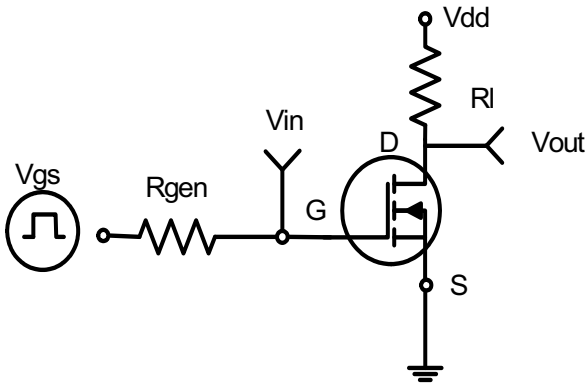


Figure 1: Switching Test Circuit

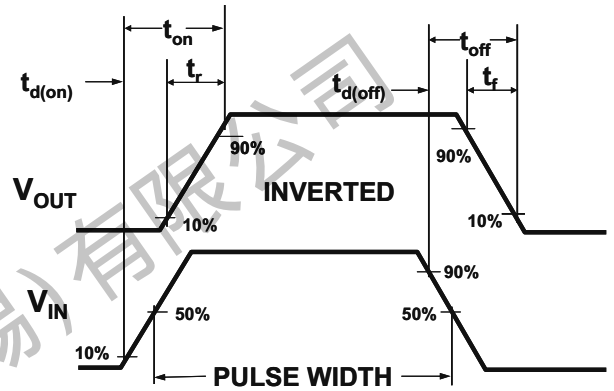


Figure 2: Switching Waveforms

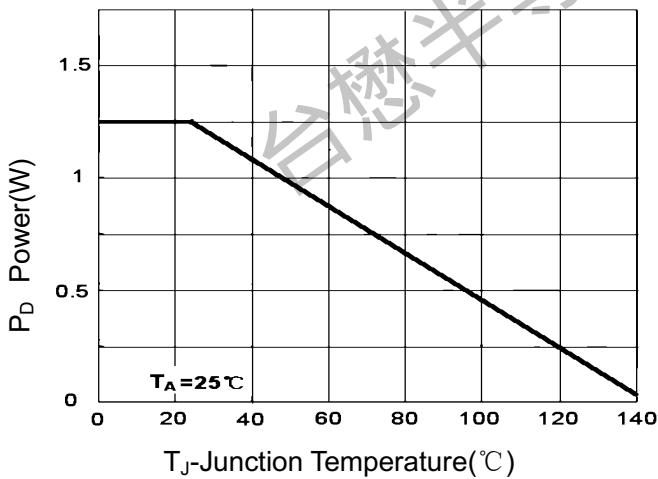


Figure 3 Power Dissipation

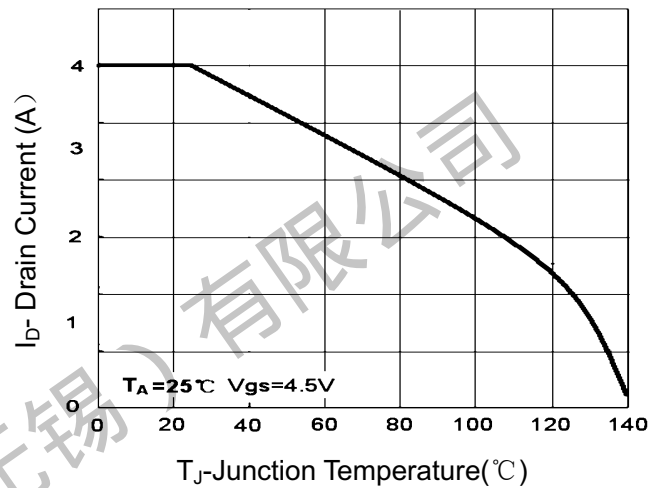


Figure 4 Drain Current

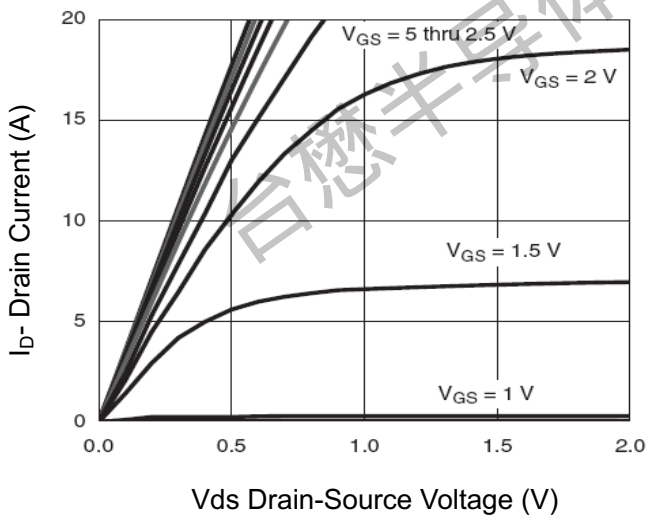


Figure 5 Output Characteristics

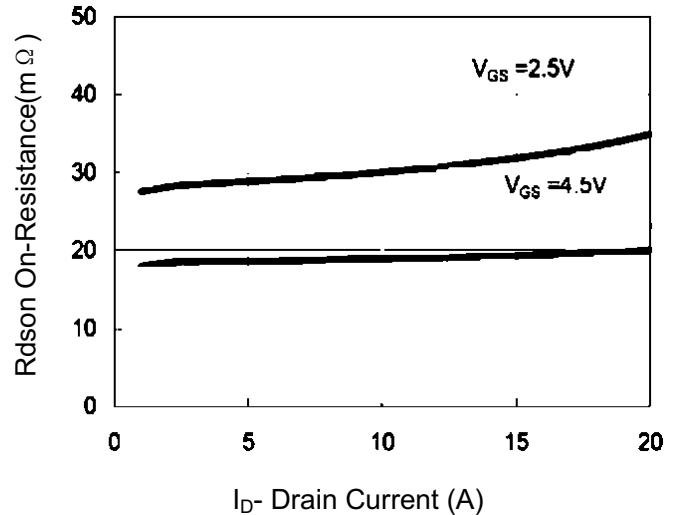


Figure 6 Drain-Source On-Resistance

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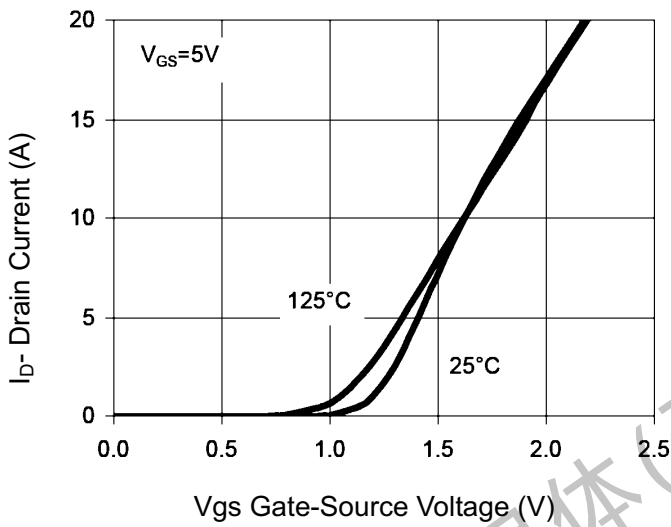


Figure 7 Transfer Characteristics

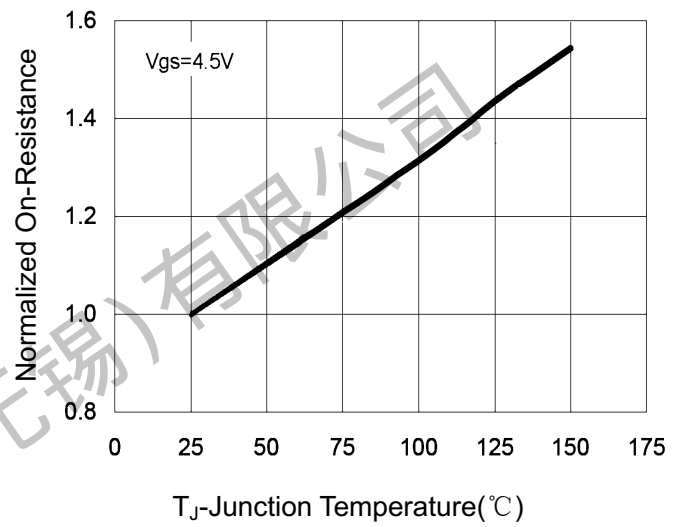


Figure 8 Drain-Source On-Resistance

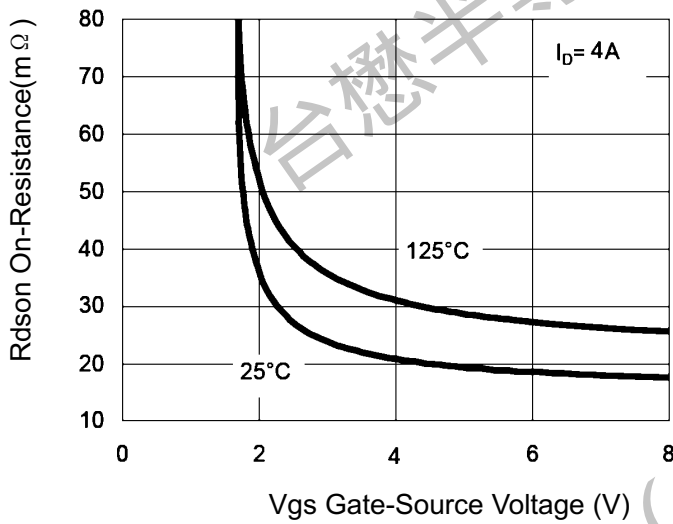


Figure 9 Rdson vs Vgs

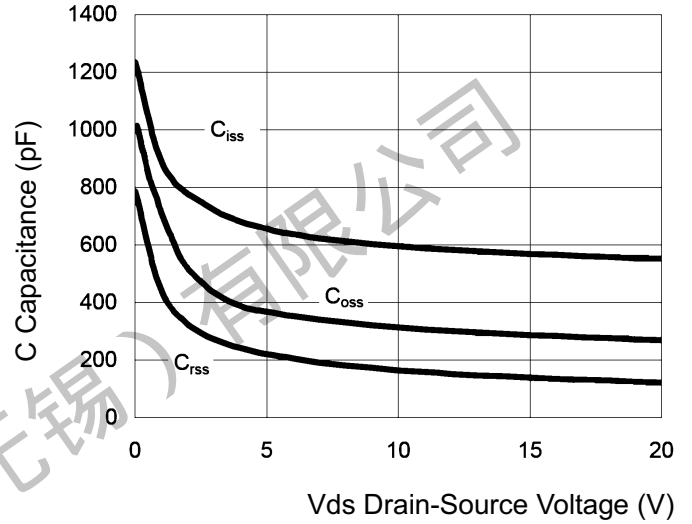


Figure 10 Capacitance vs Vds

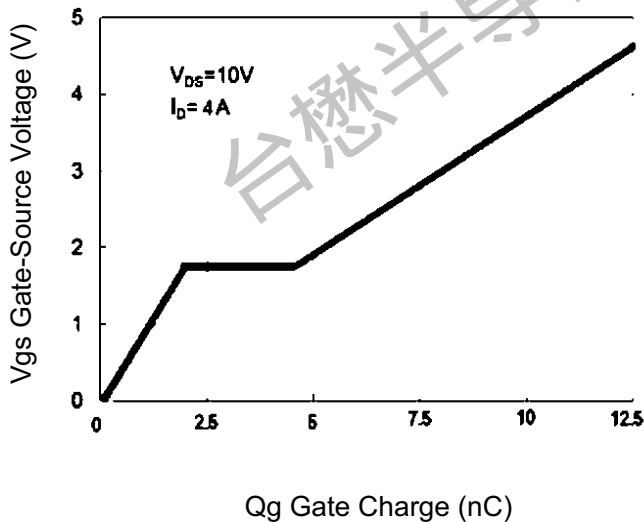


Figure 11 Gate Charge

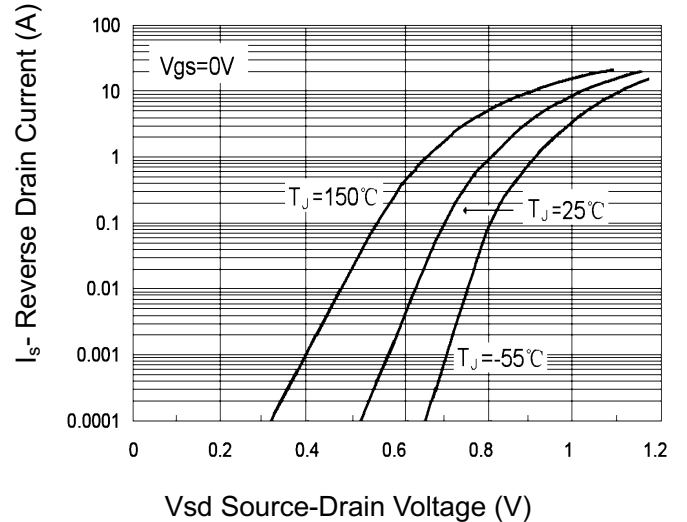


Figure 12 Source- Drain Diode Forward

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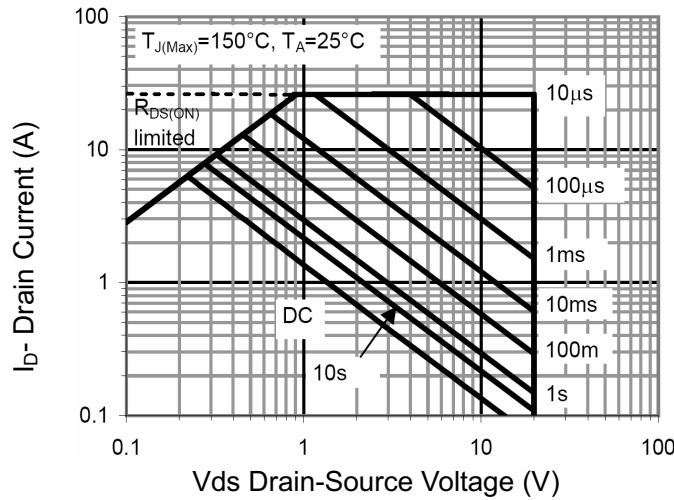


Figure 13 Safe Operation Area

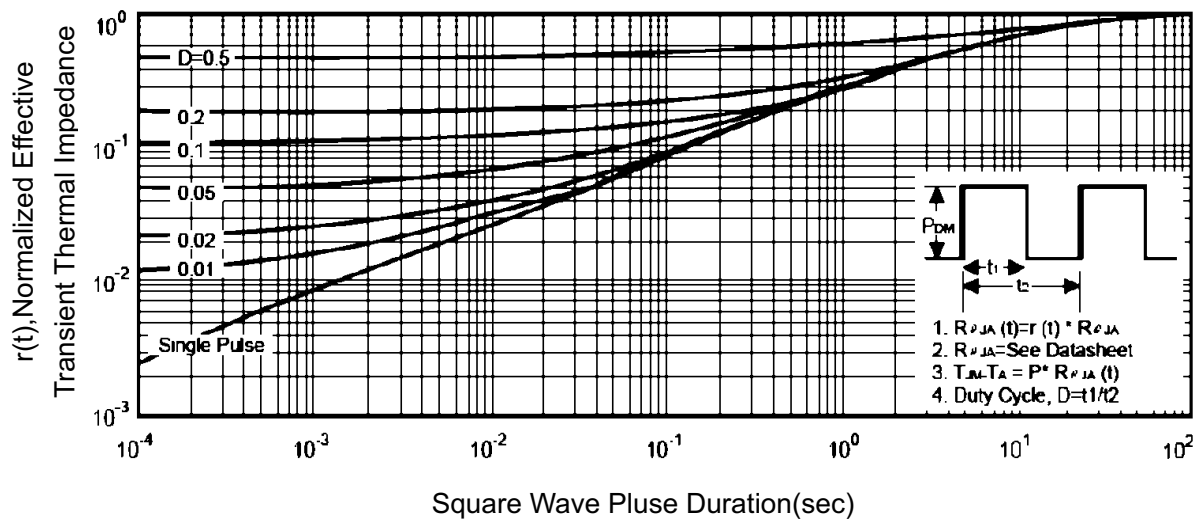
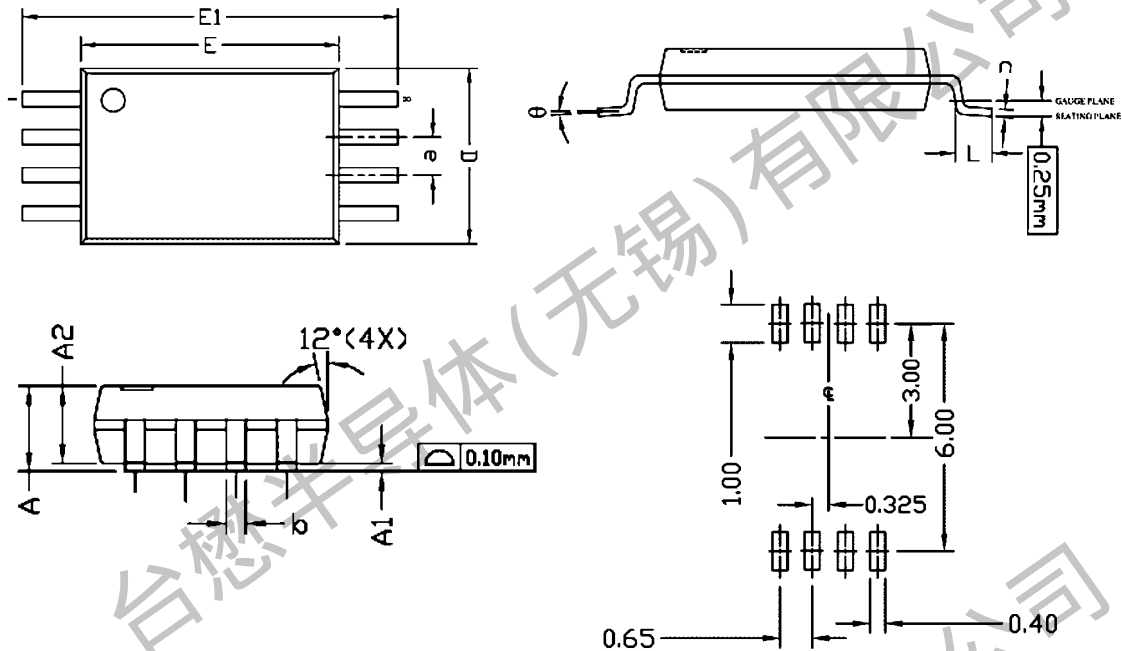


Figure 14 Normalized Maximum Transient Thermal Impedance

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Package Mechanical Data: TSSOP-8L



Symbol	Common		
	mm		
	Min	Nom	Max
A	/	/	1.20
A1	0.05	/	0.15
A2	0.80	1.00	1.05
b	0.19	/	0.30
c	0.09	/	3.45
D	2.90	3.00	3.1
E1	6.40BSC		
E	4.30	4.40	4.50
E	0.65BSC		
L	0.45	0.60	0.75
Φ	0°	0.48	8°

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Revision history:

Date	Rev	Description	Page
2023.04.21	23.04	Original	