
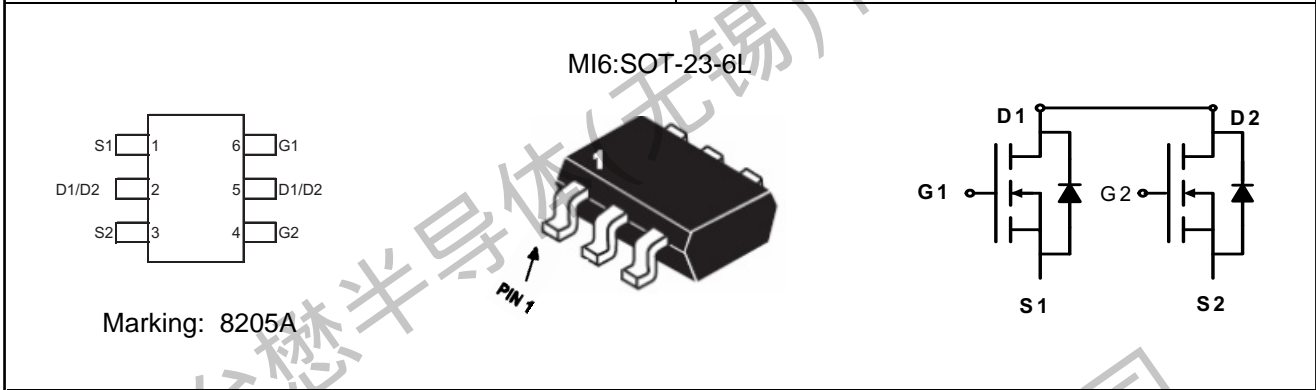


**TM07H02MI6**

**N+N-Channel Enhancement Mode Mosfet**

<p><b>General Description</b></p> <ul style="list-style-type: none"> <li>• Low <math>R_{DS(ON)}</math></li> <li>• RoHS and Halogen-Free Compliant</li> </ul> <p><b>Applications</b></p> <ul style="list-style-type: none"> <li>• Load switch</li> <li>• PWM</li> </ul>	<p><b>General Features</b></p> <p><math>V_{DS} = 20V</math> <math>I_D = 7.0A</math>  <math>R_{DS(ON)} = 19m\Omega</math> (typ.) @ <math>V_{GS}=4.5V</math></p> <p>100% UIS Tested          100% <math>R_g</math> Tested</p> 
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**Absolute Maximum Ratings ( $T_c=25^\circ C$  unless otherwise noted)**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current, $V_{GS}$ @ 4.5V	7.0	A
$I_D @ T_A=70^\circ C$	Continuous Drain Current, $V_{GS}$ @ 4.5V	4.2	A
$I_{DM}$	Pulsed Drain Current	25	A
$P_D @ T_A=25^\circ C$	Total Power Dissipation	1.25	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	125	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction Case	---	---	$^\circ C/W$

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**Electrical Characteristics** (T<sub>J</sub>=25°C unless otherwise specified)

Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	0.7	0.9	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	-	19	24	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3A	-	25	32	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =4A	-	10	-	S
<b>Dynamic Characteristics</b> (Note4)						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =8V, V <sub>GS</sub> =0V, F=1.0MHz	-	600	-	PF
Output Capacitance	C <sub>OSS</sub>		-	330	-	PF
Reverse Transfer Capacitance	C <sub>RSS</sub>		-	140	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =10V, I <sub>D</sub> =1A V <sub>GS</sub> =4V, R <sub>GEN</sub> =10Ω	-	18	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	43	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	20	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =4A, V <sub>GS</sub> =4.5V	-	11	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	2.3	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	2.5	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =2A	-	0.8	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	7	A

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Typical Performance Characteristics

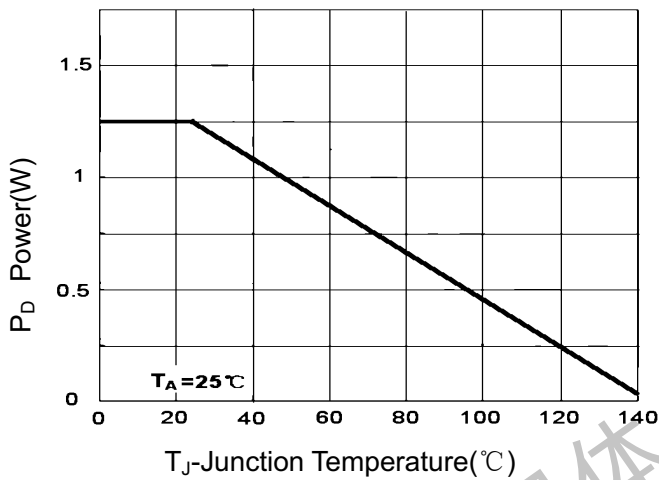


Figure 1 Power Dissipation

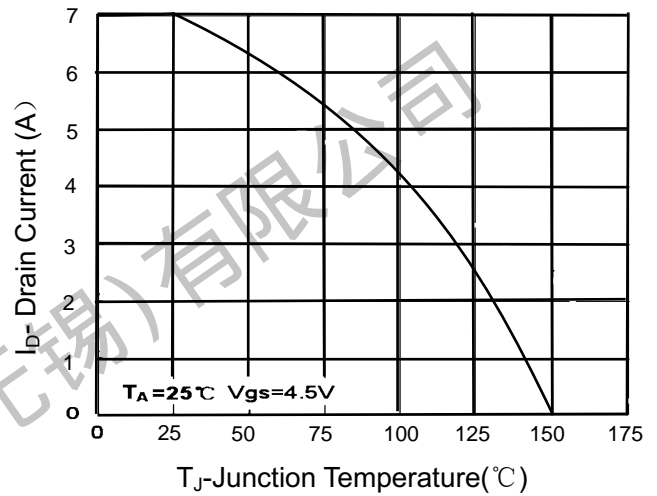


Figure 2 Drain Current

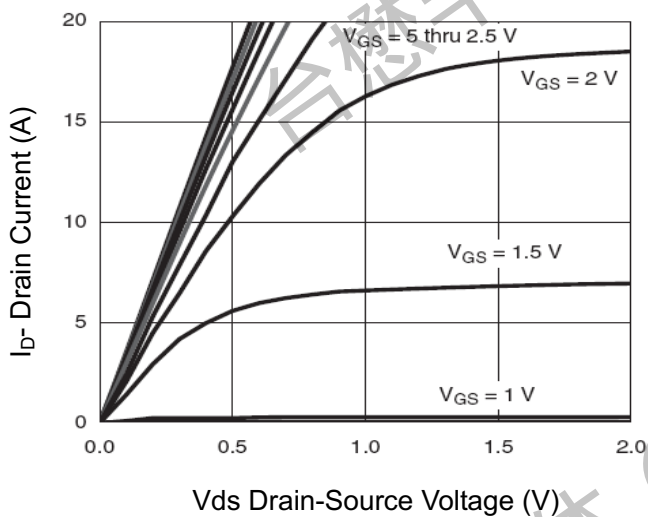


Figure 3 Output Characteristics

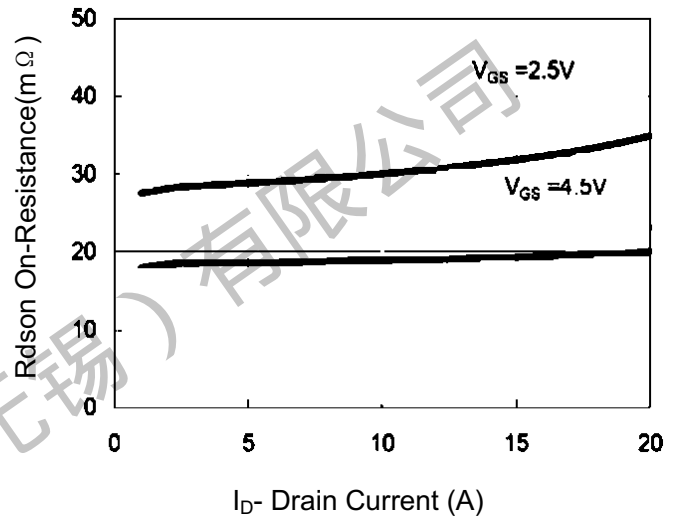


Figure 4 Drain-Source On-Resistance

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N+N-Channel Enhancement Mode Mosfet

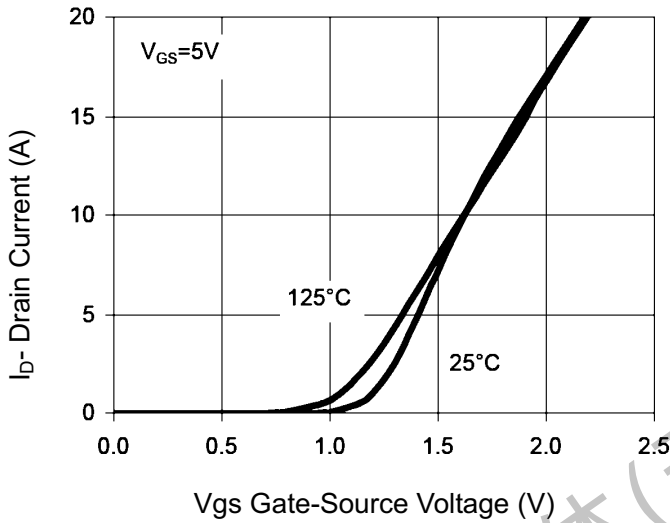


Figure 5 Transfer Characteristics

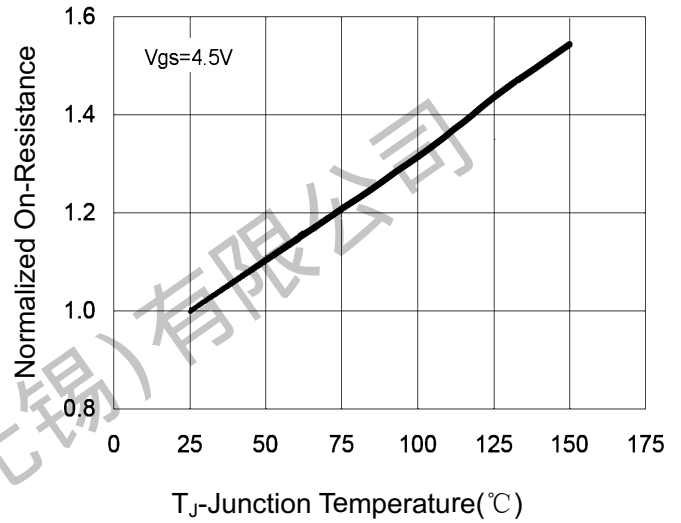


Figure 6 Drain-Source On-Resistance

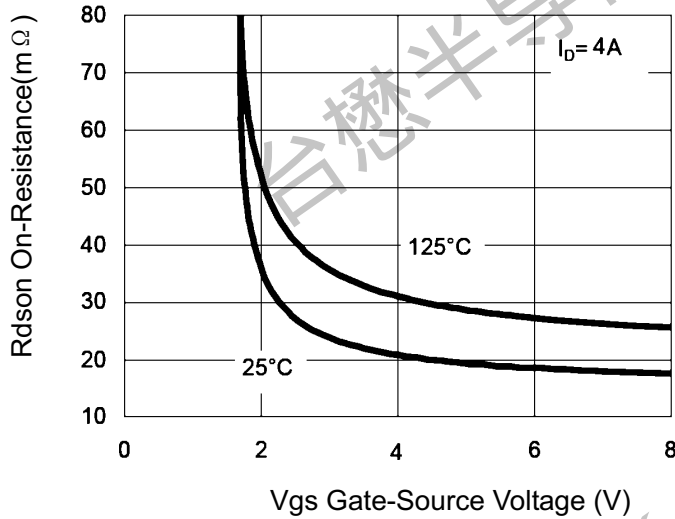


Figure 7 Rdson vs Vgs

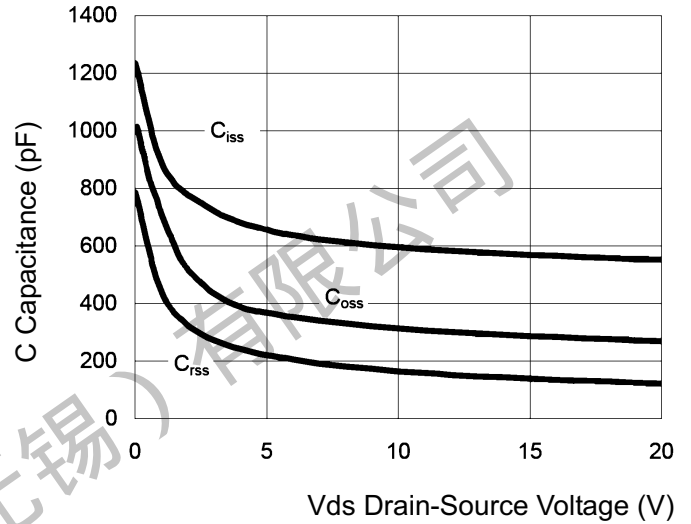


Figure 8 Capacitance vs Vds

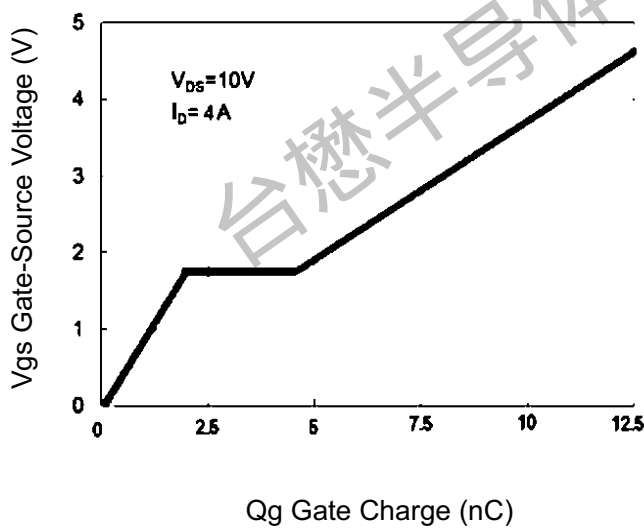


Figure 9 Gate Charge

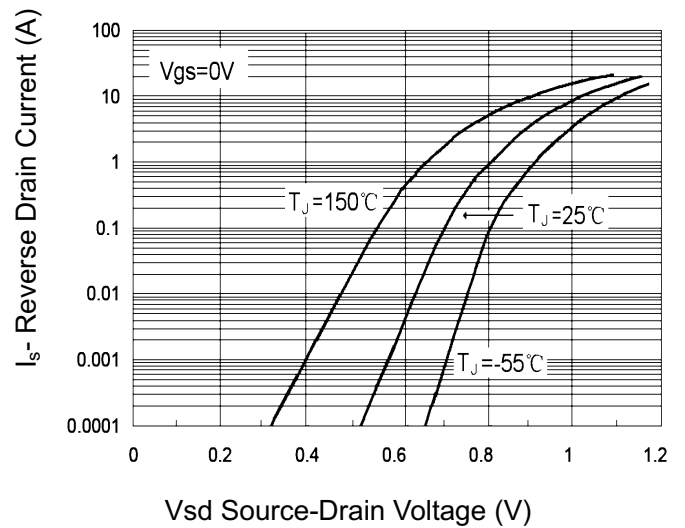


Figure 10 Source- Drain Diode Forward

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N+N-Channel Enhancement Mode Mosfet

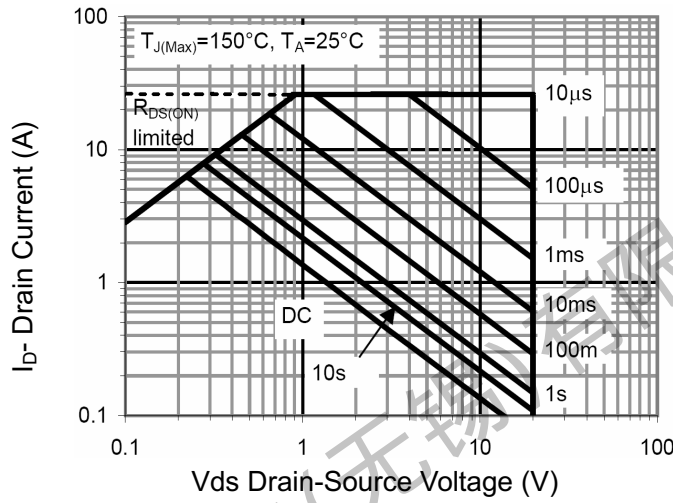


Figure 11 Safe Operation Area

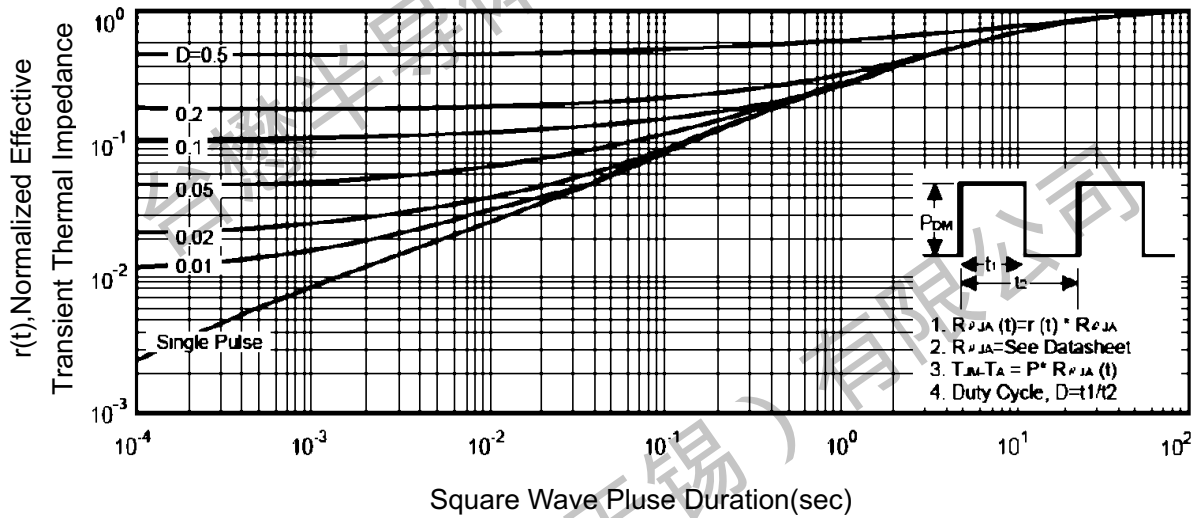


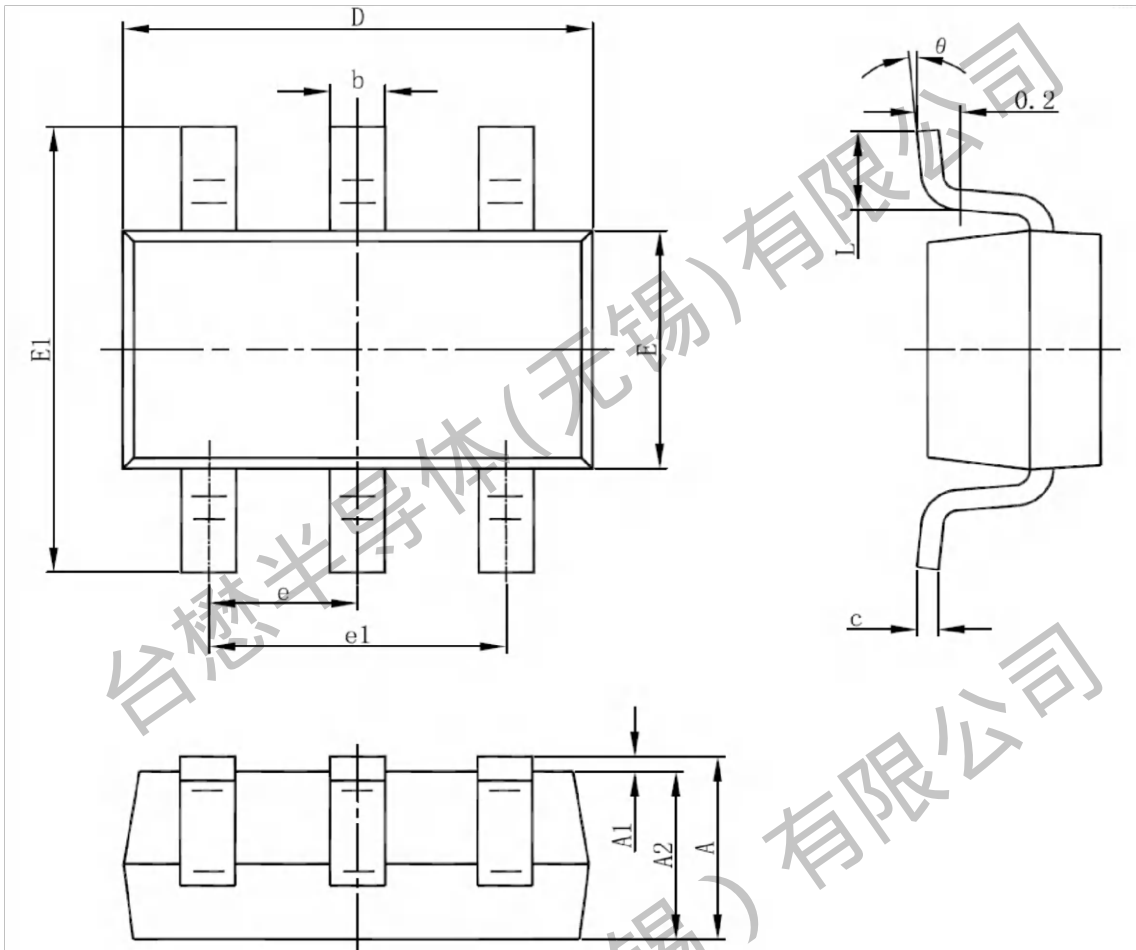
Figure 12 Normalized Maximum Transient Thermal Impedance



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**Package Mechanical Data:SOT-23-6L**

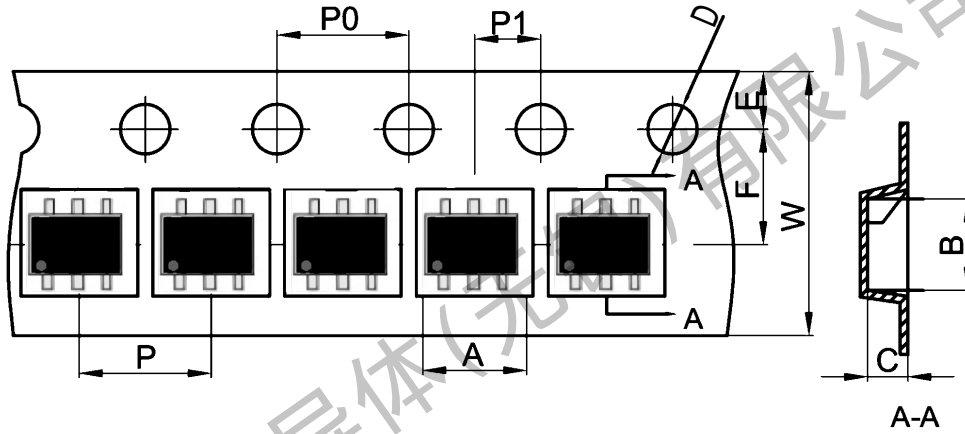


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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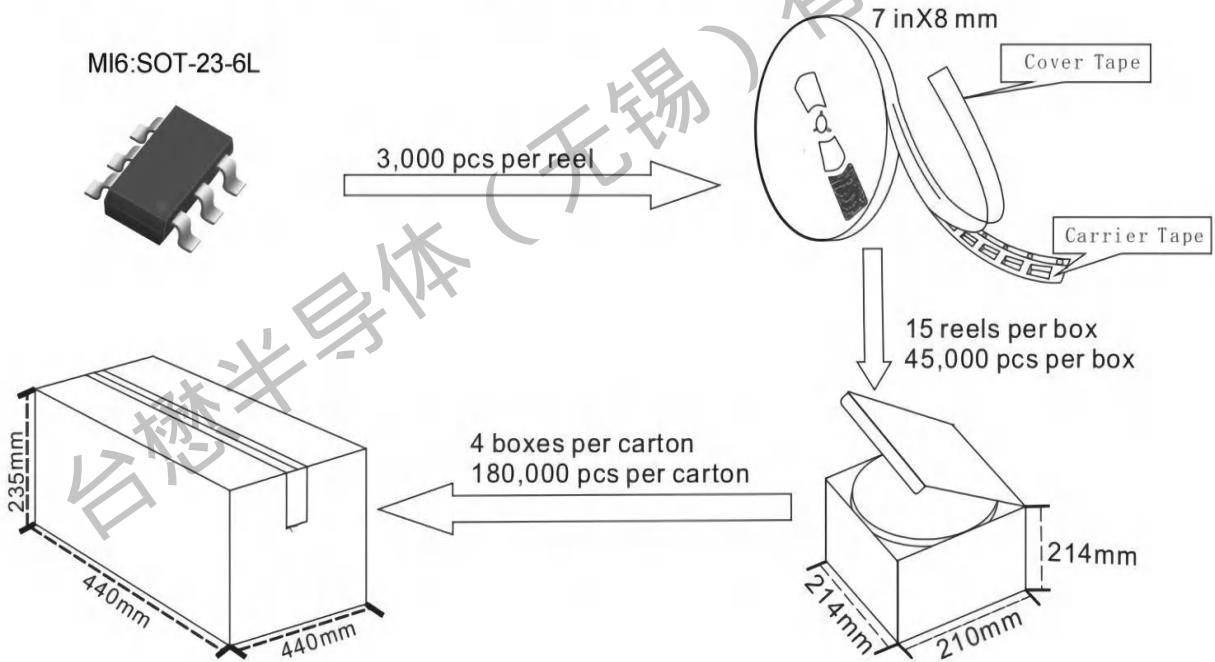
SOT-23-6L Embossed Carrier Tape



Dimensions are in millimeter										
Pkg type	A	B	C	D	E	F	P0	P	P1	W
SOT-23-6L	3.15	2.77	1.22	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00

SOT-23-6L Packing

The method of packaging and dimension are shown as below figure. (Dimension in mm)



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Revision history:

Date	Rev	Description	Page
2023.08.22	23.08	Original	